R.M.D. ENGINEERING COLLEGE
(AN AUTONOMOUS INSTITUTION)
R.S.M. NAGAR, KAVARAIPETTAI – 601206

Mandatory Disclosure (as on 26.03.2025)

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ANNEXURE – 18

Mandatory Disclosure:	**26th March 2024**
AICTE File No	F.No. Southern/1-43661119657/2024/EOA
Date & Period of last approval	19-May-2024, 2024-25
UGC Autonomous Approval	File No. F.22-1/2017(AC) Dated 22.12.2020
Anna University Autonomous Approval	Letter No. 3490/AU/CAC/Autonomous/2021 Dated 01.03.2021

1. Name of the Institution

Name of the Institution	**R.M.D. Engineering College (Autonomous)**
Address of the Institution	R.S.M. Nagar Kavaraipettai Gummidipoondi Taluk Thiruvallur District 601 206 Tamil Nadu
Phone number with STD code	044-67919191; 044-33303030
FAX number with STD code	044-67919190
E-Mail	principal@rmd.ac.in
Website	www.rmd.ac.in

2. Name and Address of the Trust

Name of the Trust	**Sri Swaminatha Naidu Educational Trust**
Address of the Trust	Plot No. 2981, “Z” Block, 1st Street, 13th Main Road Anna Nagar, Chennai – 600 040.
Phone number with STD code	044-26211504

3. Name and Address of the Principal

Name of Principal	**Dr. ANBUCHEZHIAN N**
Phone number with STD code	044-67919104; 33303031
FAX number with STD code	044-67919190

E-Mail	principal@rmd.ac.in

4. Name of the affiliating University

Name of the affiliating University	**Anna University Chennai**
Address	Chennai – 600 025
Website	www.annauniv.edu.in
Latest affiliation period	2024-2025

5. Governance

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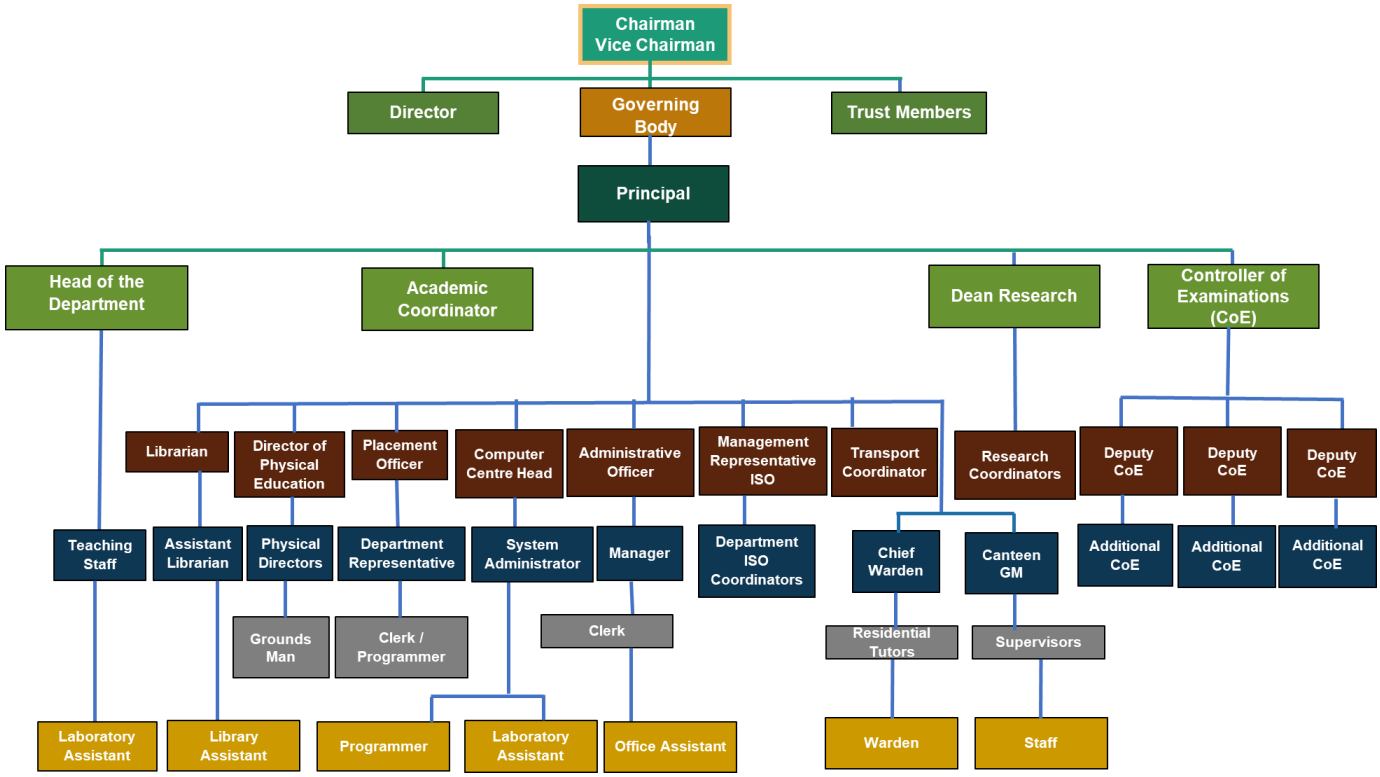
Organization chart and processes



Grievance redressal mechanism for faculty, staff and students

Complaint and Grievance Procedures

The purpose of this procedure is to establish a process for students to express and resolve misunderstandings, concerns, or grievances they have with any college employee in a prompt, fair and equitable manner. This procedure emphasizes an informal resolution.

- to protect each student's freedom of expression in the classroom

- to protect each student from prejudice or arbitrary and capricious academic evaluation as evidenced by the student's final course grade.
- to protect each student against improper disclosure of the student's views, beliefs and political associations.
- to protect a student's right to a learning environment that is free from unlawful discrimination.

Withdrawal of grievance

At any time during the grievance procedure the student may withdraw the grievance.

Time limits on filing a grievance.

A student must file a grievance by the last day of the academic semester following the action that gives rise to the grievance. Principal may suspend this rule under exceptional circumstances. All grievance records will be held in the Office of the Convenor for a period of one year.

Composition of the Grievance Redressal Committee

Members of the Grievance Redressal Committee shall be assigned as follows,

- One (1) administrator, assigned by the college Principal. The administrator will act as the committee chair.
- Two (2) faculty members, appointed by the Principal two (2) students, appointed by the Concerned department HOD.

Always follow appropriate procedures in pursuing any grievance. College rules prohibit the following:

- Obstruction or disruption of teaching, research, administration, disciplinary procedures, or other college activities. Disruption or obstruction mean conduct which disturbs, interferes with, or prevents normal campus functions and activities. Examples include creating a disturbance by yelling, using profanity, or verbally intimidating or abusing others; or making excessive or unreasonable demands for immediate action, such as demanding an appointment or a response to a grievance on the spot.

- Failure to comply with directions of a college official or resisting or obstructing such officials in the performance of their duties.

Scope and coverage

A grievance may arise when a graduate student believes that his/her status as a graduate student, or University appointment based on student status, has been adversely affected by an incorrect or inappropriate decision or behavior. Examples include, but are not limited to the following:

1. Inappropriate application of a **department/unit** or University policy.
2. Being unfairly assessed on a preliminary examination.
3. Being improperly terminated from a program.
4. Being required to perform personal services unrelated to academic or assistantship duties.

Being the subject of professional misconduct by a student's graduate supervisor or other faculty or staff member

Establishment of Anti Ragging Committee:

Members	Designation / Dept	Designation	Contact Nos.	E-Mail ID
Dr. ANBUCHEHIAN N	Principal	Chairman	9790670444	principal @rmd.ac.in
Mr. INSPECTOR OF POLICE	Police Inspector	Member	* 044-27925561	tnpudhayap@gmail.com
Mr. KRISHNASAMY R	Revenue Officer	Member	9444454884	rathann ush@gm ail.com
Mr. REVENUE DIVISIONAL OFFICER	RDO	Member	9445000410	pnirdo.t ntlr@nic.in
Dr. PAVAI MADHESWARI S	HOD SCIENCE AND HUMANITIES	Member	4433303336	hod.sh@ rmkec.a c.in
Mr. MURALI S	Parent-Rep	Member	9840149020	muralis7 0@gmail.com
Ms. Abirami V S	Student - Rep	Member	044-67919141	ucs2041 8@rmd.a c.in
Ms Jayakeerthana S	Student - Rep	Member	044-67919141	2110405 9@rmd.a c.in
Mr. GANESAN R	Administrative Officer	Member	9789534121	ao@rmd. ac.in
Dr. Helenprabha K	Professor and Head	Member	9940351280	hodece@ rmd.ac.i n
Dr. Thyagharajan K.K.	Dean RESEARCH	Member	9444112579	deanres earch@r md.ac.in
Dr. VENKATA SUBBU RAJU	Medical Officer	Member	9443242357	drvsrmb bs@gma il.com
Dr. TAMIL SELVI V	Professor and Head	Member	9444551371	hodSnh @rmd.ac.in

Establishment of Online Grievance Redressal Mechanism:

https://rmd.ac.in/Grievance/index.html

Establishment of Grievance redressal Committee in the Institution and the Appointment of OBUDSMAN by the University

Name	Designation / Department	Position in the Committee	Contact Nos.
Dr.Anbuchezhian N	**Principal**	**Chairman**	**044 - 67919104**
Dr. Balasubadra K	Professor and Head-IT Dept	Member	9865041012
Dr.Amudha G	Professor and Head-CSBS Dept	Member	9789821161
Dr.Tamil Selvi V	Professor and Head – S&H Dept	Member	9444551371
Dr. S. Sasikumar	Professor, Dept of CSE	Memeber	9441515894
Ms.Abirami V S	IV Year, B.E. Electronics & Commn Engg.	Special Invitee	044-67919141

***Ombudsman :** The Director, Centre for Student Affairs, AU as appointed by the Anna University

Establishment of Internal Complaints Committee (ICC)

Members	**Designation / Dept**	**Designation**	**Contact Nos.**	**E-Mail ID**
Dr.K. Balasubadra	Professor & HOD/IT	Presiding Officer	9865041012	hodit@rmd.ac.in
Dr.R. Priya	Professor	Member	9444765290	drpriya.snh@rmd.ac.in
Dr.A. Sumaya Begum	Associate Professor	Member	9790996037	asb.ece@rmd.ac.in
Mr.R.Ganesan	Administrative Officer	Member	9789534121	ao@rmd.ac.in
Mrs. Maria Antony Swapna	Placement Officer	Member	9940561176	tnp@rmd.ac.in
Ms. Jayakeerthana S	Student – Rep. (IV – ECE -111521104059)	Member	044-67919140	21104059@rmd.ac.in
Ms. Rapuru Manvitha	Student – Rep. (IV – IT -11152120340)	Member	044-67919160	2120340@rmd.ac.in
Mr. Karthikeyan R	Student – Rep. (III – CSBS 111522202020)	Member	044-67919153	21222020@rmd.ac.in
Mr.R.Krishnasamy	Advocate / High Court	Member	9444454884	rathanaanush@gmail.com

Establishment of Committee for SC/ST

Members	Designation / Dept	Coordinator/ Member	Contact Nos.	E-Mail ID
Dr.Anbuchezhian N	Principal	Convenor	9655566926	principal@rmd.ac.in
Dr.K.HelenPrabha	Professor & HOD-ECE	Member / Registry Incharge	9345036915	hod.ece@rmd.ac.in
Dr.K. Balasubadra	Professor	Member	9865041012	hodit@rmd.ac.in
Mrs. P. Poonkuzhali	Associate Professor	Member	9444060143	poonkuzhali.ece@rmd.ac.in
Dr.PavaiMadheswari	NGO	Member	8754033336	ac@rmkec.ac.in
Mr.R.Ganesan	Administrative Officer	Member	9789534121	ao@rmd.ac.in
Mr.R.Krishnasamy	Advocate /High Court	Member	9444454884	rathanaanush@gmail.com

Internal Quality Assurance Cell (IQAC)

S.No.	Name of the Faculty	Designation	Composition criteria specified by NAAC	Position in IQAC
1.	Thiru R.S. Munirathinam	Founder Chairman	Management Member	Member
2.	Shri. R.M. Kishore	Vice Chairman	Management Member	Member
3.	Thiru. R. Jothi Naidu	Managing Director, Sri Ganapathy Bricks Industries, Thiruvalluvar colony, Anna Nagar, Chennai-40	Industrialist	Member
4.	Dr.M.S.Palanichamy	Former Vice Chancellor, Tamil Nadu Open University	Educationist & Local Society	Member
5.	Mr. Parthasarathy	Assistant Manager, Newgen Software Technologies Pvt. Ltd, Chennai.	Employer	Member
6.	Dr. S. Venkateswari	Department of CSE, L.N Government Arts College, Ponneri	Stake Holder-Parent	Memebr
7.	Dr.K.K.Thyagharajan	Dean-Research	Senior Faculty	Member
8.	Dr.P.Ezhumalai	Professor & Head / CSE	Senior Faculty	Member
9.	Dr. K. HelenPrabha	Professor & Head / ECE	Senior Faculty	Member
10.	Dr. G. Amudha	Professor & Head / CSBS	Senior Faculty	Member
11.	Dr. C S Anita	Professor & Head / AI&ML	Senior Faculty	Member
12.	Dr. V. Tamilselvi	Professor & Head / S&H	Senior Faculty	Member

S.No.	Name of the Faculty	Designation	Composition criteria specified by NAAC	Position in IQAC
13.	Dr.A. Chilambuchelvan	Professor /ECE & Controller of Examinations	Senior Faculty	Member
14.	Dr.D.RukmaniDevi	Professor / ECE	Senior Faculty	Member
15.	Dr. R. Priya	Professor/ S&H	Senior Faculty	Member
16.	Dr.S.Muthusundari	Associate Professor / Dept. IQAC Coordinator/ CSE (ISO-MR)	Faculty	Member
17.	Dr. C. Shobana Nageswari	Associate Professor / Dept. IQAC Coordinator/ ECE	Faculty	Member
18.	Dr. N. Muthuvairavan Pillai	Associate Professor / Dept. IQAC Coordinator/ CSBS	Faculty	Member
19.	Mrs, Remya Rose S	Assistant Professor / Dept. IQAC Coordinator/ AIML	Faculty	Member
20.	Dr.R.Jothilakshmi	Associate Professor / Dept. IQAC Coordinator/IT	Faculty	Member
21.	Dr.G.Ganapathy	Assistant Professor / Dept. IQAC Coordinator/ S& H	Faculty	Member
22.	Dr. S. Shalini	Assistant Professor / Dept. IQAC Coordinator/ S& H	Faculty	Member
23.	Dr. N. Padmavathy	Assistant Professor / Dept. IQAC Coordinator/ S& H	Faculty	Member
24.	Dr. G. Gayathri Devi	Assistant Professor / S&H Covener- Higher Education Cell	Faculty	Member
25.	Dr. C. Benniala Thangammal	Professor/ECE, Converner- Institutions Innovation Council & Business Incubator	Faculty	Member
26.	Mr.R.Ganesan	Administrative Officer	Senior Administrative Officer	Member
27.	Ms.Maria Swapna Antony	Placement Officer	Administration	Member
28.	Ms. S. Sharmila	III Year Student / AI&ML	Student Nominee	Member
29.	Ms. B. Praveena	Assistant Professor RMKEC / EIE	Alumni Nominee	Member
30.	Dr. K. Balasubadra	Professor & Head / IT	Senior Faculty	Coordinator
31.	Dr. N.Anbuchezhian	Principal	Head of the Institution	Chairperson

Equal Opportunity facilities Cell.

An Equal Opportunity comprising of the following persons is hereby established . The Committee shall ensure implementation of the schemes and programmes as devised by the Union / State Government for the students / staff belonging to **WOMEN/SC/ST/DIFFERENTNLY ABLED/MINORITY AND OBC COMMUNITY,** as well as will look into their welfare.

Name of the person	**Designation**	**Position in Committee**
Dr.N.Anbuchezhian	Principal	Chairman
Dr.K.Helen Prabha	Prof. & Head – Dept of ECE	Coordinator
Dr.A.Sumaiya Begum	Professor – Dept of ECE	Member
R.Ganesan	Administrative Officer	Member

6. Programmes

Name of Programmes Approved by AICTE

Sl.No.	Degree	Name of the Programme
1.	B.E.	Computer Science and Engineering
2.	B.E.	Electronics and Communication Engineering
3.	B.Tech.	Information Technology
4.	B.Tech.	Computer Science and Business Systems
5.	B.Tech.	Artificial Intelligence and Machine Learning

Name of Programmes Accredited by NBA

Sl.No.	Degree	Name of the Programme
1.	B.E.	Computer Science and Engineering
2.	B.E.	Electronics and Communication Engineering
3.	B.Tech.	Information Technology

Status of NBA Accreditation of the Courses

Total no. Of Courses : **05**

Total no. Of Accredited Courses: **03 (All the eligible U.G Courses are accredited)**

Course	Year of Introduction	Accreditation status	Period of accreditation	Letter No. and Date
B.E. Computer Science & Engineering	2001	Accredited	2023-2024 to 2025-2026 i.e., upto 30.06.2026	F.No.33-243/2010-NBA Dated 16.02.2024
B.E. Electronics & Communication Engineering	2001			
B.Tech. Information Technology	2001			
B.Tech Computer Science and Business Systems	2020	Not Eligible for Accreditation	-	-
B.Tech Artificial Intelligence and Machine Learning	2021	Not Eligible for Accreditation	-	-

Status of NAAC Accreditation

Status of Accreditation	Accreditation Grade
Accredited	B++

Name / No. Of Seats / Duration of the programmes

Sl. No.	Degree	Name of the Programme	Sanctioned intake in the academic year 2023 -2024	Duration of the Course (In years)
1.	B.E.	Computer Science and Engineering	240	4
2.	B.E.	Electronics and Communication Engineering	180	4
3.	B.Tech.	Information Technology	120	4
4.	B.Tech.	Computer Science and Business Systems	60	4
5.	B.Tech.	Artificial Intelligence and Machine Learning	60	4

Cut off marks/rank of admission during the last three years.

Sl.No.	Degree	Name of the Programme	Academic Year		
			2024-2025	2023-2024	2022-2023
1	B.E.	Computer Science and Engineering	198.65 – 105.34	190.00 – 124.00	196.00 -136.00
2	B.E.	Electronics and Communication Engineering	198.66 – 104.66	186.00 – 125.30	186.66 - 134.00
3	B.Tech.	Information Technology	180.00 – 107.34	184.66 – 132.00	188.00 -149.34
4	B.Tech.	Computer Science and Business Systems	180.00 – 112.00	182.00 – 128.66	182.66 - 146.00
5	B.Tech.	Artificial Intelligence and Machine Learning	198.00 – 116.66	186.00 – 134.00	183.34 - 116.00

Fee details during last three years

Sl.No.	Degree	Name of the Programme	Fee in Rs. (As per Govt Norms)		
			2024-2025	2023-2024	2022-2023
1.	B.E.	Computer Science and Engineering	Rs. 55,000/- (All Accredited Courses)	Rs. 55,000/- (All Accredited Courses)	Rs. 55,000/- (All Accredited Courses)
3.	B.E.	Electronics and Communication Engineering			
4.	B.Tech.	Information Technology			
5.	B. Tech.	Computer Science and Business System	Rs.50,000/-	Rs.50,000/-	Rs.50,000/-
6	B. Tech.	Artificial Intelligence and Machine Learning	Rs.50,000/-	Rs.50,000/-	Rs.50,000/-

7.Faculty

Branch wise list of Faculty Members

DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING

S. No.	Name	Designation
1	Dr. EZHUMALAI P	Professor & Head
2	Dr. SASIKUMAR R	Professor
3	Dr. SRINIVASAN S	Professor
4	Dr. JOE PRATHAP PM	Professor
5	Dr. HEMALATHA M.	Professor
6	Dr. VEDARAJ M	Professor
7	Dr. MUTHUSUNDARI	Assoc. Professor
8	Dr. GNANA SEKAR A	Assoc. Professor
9	Dr. RAJALAKSHMI D	Assoc. Professor
10	Dr. MURUGESAN S	Assoc. Professor
11	Dr. RAM SHANKAR N	Assoc. Professor
12	Dr. JAITHUNBI A.K.	Assoc. Professor
13	Dr. NIRMALA G	Assoc. Professor
14	Dr. ROSLIN DAYANA K	Assoc. Professor
15	Mr. MOHANA SUNDARAM K	Asst. Professor
16	Ms. VISHNU SAKTHI D	Asst. Professor
17	Mr. JAYAKUMAR D	Asst. Professor
18	Ms. GEETHA PRIYA J	Asst. Professor
19	Ms. BALASARANYA K	Asst. Professor
20	Ms. TAMIZHARASI A	Asst. Professor
21	Ms. PADMAPRIYA K	Asst. Professor
22	Ms. LOGESSWARI S.	Asst. Professor
23	Ms. SHARMILA V	Asst. Professor
24	Ms. MANISHA G	Asst. Professor
25	Ms. SHERIN BEEVI L.	Asst. Professor
26	Ms. SHERINE GLORY J	Asst. Professor
27	Ms. GEETHA PRIYA S	Asst. Professor
28	Mr. SHANKAR G	Asst. Professor
29	Mr. JAGADEESAN P	Asst. Professor
30	Ms. STERLIN RANI D	Asst. Professor
31	Ms. KALAISELVI D M	Asst. Professor
32	Ms. SUHASINI S	Asst. Professor
33	Mr. KRISHNA K E	Asst. Professor
34	Ms. MOHANA PRIYA M P	Asst. Professor

DEPARTMENT OF ELECTRONICS AND COMMUNICATON ENGINEERING

S. No.	Name	Designation
1	Dr. THYAGHARAJAN K K	Dean-Research
2	Dr. HELEN PRABHA K	Professor & Head
3	Dr. RUKMANI DEVI D	Professor
4	Dr. CHILAMBUCHELVAN A	Professor
5	Dr. BENNILA THANGAMMAL C	Professor
6	Dr. SUMAIYA BEGUM A	Professor
7	Dr. JAYAUDHAYA J	Assoc. Professor
8	Dr. HYMLIN ROSE S G	Assoc. Professor
9	Dr. SHOBANA NAGESWARI C	Assoc. Professor
10	Dr. VINI ANTONY GRACE N	Assoc. Professor
11	Dr. SUMITHRA J	Assoc. Professor
12	Ms. POONKUZHALI P	Assoc. Professor
13	Mr. BALASUBRAMANI S	Assoc. Professor
14	Dr. SHAKUNTHALA M	Asst. Professor
15	Mr. JYOTHI PRASAD M	Asst. Professor
16	Ms. HEMALATHA R	Asst. Professor
17	Mr. JAI GANESH B	Asst. Professor
18	Mr. BHARATHI DHASAN D	Asst. Professor
19	Ms. SANTHOSHINI P	Asst. Professor
20	Mr PRABHU V S	Asst. Professor
21	Mr. KARUPPAIAH S	Asst. Professor
22	Mr. JAGAN BABU J	Asst. Professor
23	Mrs. GAYATHRI PRIYA S	Asst. Professor
24	Ms. JAYANTHI S.	Asst. Professor
25	Ms. ANITHA G	Asst. Professor
26	Ms. SENTHIL PRIYA R M	Asst. Professor
27	Ms. NISHANTHI K.S.	Asst. Professor
28	Ms. DEVI PRIYA L.	Asst. Professor

DEPARTMENT OF INFORMATION TECHNOLOGY

S. No.	Name	Designation
1	Dr. BALASUBADRA K	Professor & Head
2	Dr. PRASANNA SRINIVASAN V	Professor
3	Dr. JOTHILAKSHMI R	Assoc. Professor
4	Dr. KALPANA B	Assoc. Professor
5	Dr. PRAVEENA D	Assoc. Professor
6	Dr. SARAVANAN.K	Assoc. Professor
7	Dr. VELMURUGAN S	Assoc. Professor
8	Ms. RADHIKA M.	Asst. Professor
9	Ms. ABIRAMI N	Asst. Professor
10	Ms. JANANI G	Asst. Professor
11	Ms. AROCKIA ROSY N	Asst. Professor
12	Ms. DEEPIKA D.S.	Asst. Professor

DEPARTMENT OF COMPUTER SCIENCE AND BUSINESS SYSTEMS

S. No.	Name	Designation
1	Dr. AMUDHA G	Professor & Head
2	Dr. SUDHA K	Assoc. Professor
3	Dr. MUTHUVAIRAVAN PILLAI N	Assoc. Professor
4	Ms. SRI LAKSHMI C H	Asst. Professor
5	Ms. ANITHA RAJATHI	Asst. Professor
6	Ms. MONICA LAKSHMI R	Asst. Professor
7	Ms. DEEPA S	Asst. Professor
8	Ms. SASIREKHA K	Asst. Professor
9	Ms. ADAIKKAMMAI A	Asst. Professor
10	Ms. GOWRIDURGA A.	Asst. Professor

DEPARTMENT OF ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING

S. No.	Name	Designation
1	Dr. ANITA C S	Professor & Head
2	Dr. SUDHARSON K.	Assoc. Professor
3	Dr. AMBHIKA C	Assoc. Professor
4	Dr. BADI ALEKHYA	Assoc. Professor
5	Ms. MONISHA M	Assoc. Professor
6	Ms. REMYA ROSE S	Asst. Professor
7	Ms. SWETHA S	Asst. Professor
8	Mr. SATHISH KUMAR N	Asst. Professor
9	Mr. DHAKSHUNHAA MOORTHIY	Asst. Professor
10	Ms. DIVYA K	Asst. Professor
11	Ms. GAYATHRI DEVI K	Asst. Professor
12	Ms. SAVITHA S.	Asst. Professor

DEPARTMENT OF SCIENCE AND HUMANITIES

S. No.	Name	Designation
1	Dr. TAMIL SELVI V	Professor & Head
	MATHEMATICS	
2	Dr. SARAVANAN S	Assoc. Professor
3	Dr. SUMATHY K	Assoc. Professor
4	Dr. SURESH M	Assoc. Professor
5	Dr. PRABAHARAN N	Professor
6	Dr. RAJAKUMARI S	Asst. Professor
7	Dr. SURESH M	Asst. Professor
8	Dr. GANAPATHY G	Asst. Professor
9	Mr. VENKATESAN T	Asst. Professor
10	Dr. JYOTHI BALA A	Asst. Professor
11	Mr. SILAMBARASAN M	Asst. Professor
12	Dr. SUBRAMANIAN S	Asst. Professor
13	Mr. DURAIRAJ M.N.	Asst. Professor
	PHYSICS	
14	Dr. PRIYA R	Professor
15	Dr. AMUDHA K	Assoc. Professor
16	Dr. SHALINI S	Assoc. Professor
17	Dr. RANJANI R	Asst. Professor
	CHEMISTRY	
18	Dr. RADHA K S	Professor
19	Dr. RAMESH S	Assoc. Professor
20	Dr. REKHA S	Assoc. Professor

21	Dr. SUBHA V	Asst. Professor
22	Dr. KAMALARAJAN.P	Asst. Professor
	ENGLISH	
23	Dr. GAYATHIRI DEVI G	Assoc. Professor
24	Ms. VANI M V	Asst. Professor
25	Ms. MEEDPHIN ARASI M G	Asst. Professor
	GENERAL ENGINEERING	
26	Dr. USHA RANI P	Professor
27	Dr. PADMAVATHI. N	Assoc. Professor
28	Dr. SUNDARA VADIVEL T A	Assoc. Professor
29	Dr. NAGABALAN U	Asst. Professor
30	DR. ASHOK KUMAR R	Asst. Professor
31	Mr. ARUL JOTHI G	Asst. Professor
32	Ms. RAJESWARI P	Asst. Professor
33	Ms. NALINA E	Asst. Professor
34	Mr. KARTHICK MURUGAN S	Asst. Professor
35	Ms. PAVITHRA K N	Asst. Professor

Permanent Faculty: Student Ratio: 1:16.57

8. Profile of Principal

Name	Dr. ANBUCHEZHIAN N
Date of birth / Age	08-10-1965 / 58
Uniue id	1-21838 44008
Father Name	Mr. NATTAPPAN P
Date of joining	29-11-2017
Experience (Academic)	36 Years
Telephone number - Office	044 - 67919104
Telephone number - Residence	044 - 29815033
Fax number	044 - 67919190
Mobile number	9790670444
E-mail	principal@rmd.ac.in
Residential Address Line 1	FLAT NO. B 306, RMK CHOLA GARDENS, SUNDARACHOLAVARAM ROAD,
Line 2	TIRUVERKADU, AYAPPAKKAM, 600077
District	Tiruvallur

Educational Qualification	<table><tr><th>Degree</th><th>Specialization</th><th>Class</th></tr><tr><td>B.E.</td><td>Mechanical Engineering</td><td>First Class</td></tr><tr><td>M.E.</td><td>Engineering Design</td><td>First Class</td></tr><tr><td>Ph.D.</td><td>Faculty of Mechanical Engineering</td><td>Others-AWARDED</td></tr><tr><td>M.B.A.</td><td>Master of Business Administration</td><td>First Class</td></tr></table>
Title of the Ph.D. Thesis	CONJOINTANALYSISFORPRODUCTDEVELOPMENTAPPLIED TO DOMESTIC SOLAR WATERHEATERS

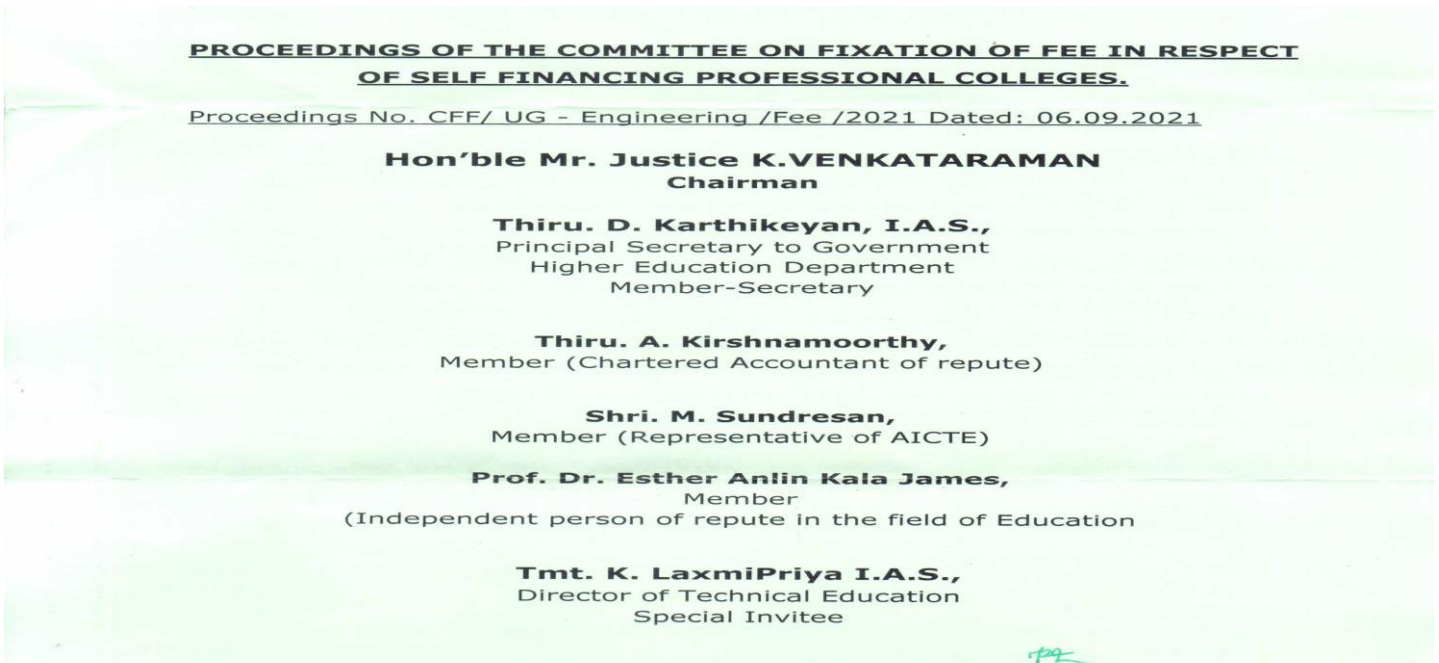
9.Fee

Details of Fee, as approved by State Fee Committee, for the institution.

Sl.No.	Degree	Name of the Programme	Fee in Rs. (As per Govt Norms)		
			2024-2025	2023-2024	2022-2023
1.	B.E.	Computer Science and Engineering	Rs. 55,000/- (All Accredited Courses)	Rs. 55,000/- (All Accredited Courses)	Rs. 55,000/- (All Accredited Courses)
3.	B.E.	Electronics and Communication Engineering			
4.	B.Tech.	Information Technology			
5.	B. Tech.	Computer Science and Business System	Rs.50,000/-	Rs.50,000/-	Rs.50,000/-
6	B. Tech.	Artificial Intelligence and Machine Learning	Rs.50,000/-	Rs.50,000/-	Rs.50,000/-

- Fees for Government Quota

Proceedings of the Fee committee (06.09.2021)



PROCEEDINGS OF THE COMMITTEE ON FIXATION OF FEE IN RESPECT OF SELF FINANCING PROFESSIONAL COLLEGES.

Proceedings No. CFF/ UG - Engineering /Fee /2021 Dated: 06.09.2021

Hon'ble Mr. Justice K.VENKATARAMAN
Chairman

Thiru. D. Karthikeyan, I.A.S.,
Principal Secretary to Government
Higher Education Department
Member-Secretary

Thiru. A. Kirshnamoorthy,
Member (Chartered Accountant of repute)

Shri. M. Sundresan,
Member (Representative of AICTE)

Prof. Dr. Esther Anlin Kala James,
Member
(Independent person of repute in the field of Education

Tmt. K. LaxmiPriya I.A.S.,
Director of Technical Education
Special Invitee

ORDER:

R.M.K Engineering College, RSM Nagar, Kavaraipettai 601 206, Gummudipoondi Tk, Thiruvallur District has sought permission to collect the fee for the new courses namely B.Tech – Computer Science and Business Systems, B.Tech – Artificial Intelligence and Data Science and B.E – Computer Science and Design.

Likewise R.M.D Engineering College, RSM Nagar, Kavaraipettai 601 206, Gummudipoondi Tk, Thiruvallur District sought permission to fix the fees for the following courses B.Tech – Computer Science and Business Systems, B.Tech – Artificial Intelligence and Machine Learning.

Also R.M.K College of Engineering and Technology, RSM Nagar, Puduvoyal - 601 206 Gummudipoondi Tk, Thiruvallur District sought for fixation of fee for the following course B.Tech – Artificial Intelligence and Data Science.

The Committee earlier considered the request made by the institutions by an order dated 30-11-2020. The following maximum fee structure was fixed for the academic year 2020 -2021, by the said order

Sl. No.	**Name of the Course**		**Fee fixed by the Committee Rs.**	**Remarks**
1.	B.E.-B.Tech/B.Arch. (Category-I) **(Govt. Quota)**	Non-Accredited Courses	**50,000/Annum**	The fee is an all inclusive annual fee including various fees like Tuition fee, Admission fee, Special fee, Laboratory / Computer / Internet fee, Library fee, Sports fee, Maintenance and Amenities fee, Extracurricular activities fee and other recurring expenditure
		Accredited Courses	**55,000/ Annum**	
2.	B.E.-B.Tech/B.Arch. (Category-II) **(Seats retained by the Colleges)**	Non-Accredited Courses	**1,40,000/Annum**	
		Accredited Courses	**1,45,000/Annum**	

Considering the said facts and circumstances we are of the view the following maximum fee structure will take effect for the students admitted in the above referred course in the colleges mentioned above for the academic year 2021 -2022.

Sl. No.	Name of the Course		Fee fixed by the Committee Rs.	Remarks
1.	B.Tech -Computer Science and Business Systems,	Non-Accredited Courses	**50,000/Annum**	The fee is an all inclusive annual fee including various fees like Tuition fee, Admission fee, Special fee, Laboratory / Computer / Internet fee, Library fee, Sports fee, Maintenance and Amenities fee, Extracurricular activities fee and other recurring expenditure
	B.Tech - Artificial Intelligence and Data Science B.Tech - Artificial Intelligence and Machine Learning B.E - Computer Science and Design (Category-I) **(Govt. Quota)**	Accredited Courses	**55,000/ Annum**	
2.	B.Tech - Computer Science and Business Systems,	Non-Accredited Courses	**1,40,000/Annum**	
	B.Tech - Artificial Intelligence and Data Science B.Tech – Artificial Intelligence and Machine Learning B.E - Computer Science and Design (Category-II) **(Seats retained by the Colleges)**	Accredited Courses	**1,45,000/Annum**	

Proceedings of the Fee committee (30.11.2020)

PROCEEDINGS OF THE COMMITTEE ON FIXATION OF FEE IN RESPECT OF SELF FINANCING PROFESSIONAL COLLEGES.

Proceedings No. CFF/ UG - Engineering /Fee /2020 Dated :30.11.2020

Hon'ble Mr. Justice K.VENKATARAMAN
Chairman

Selvi. Apoorva, I.A.S.,
Principal Secretary to Government
Higher Education Department
Member-Secretary

Thiru. A. Kirshnamoorthy,
Member (Chartered Accountant of repute)

Shri. M. Sundresan,
Member (Representative of AICTE)

Prof. Dr. Esther Anlin Kala James,
Member
(Independent person of repute in the field of Education

Thiru K. Vivekanadan, I.A.S.,
Commissioner of Technical Education
Special Invitee

ORDER:

1. The Committee was constituted for fixation of fee in respect of Self-Financing Professional colleges under the Chairmanship of Hon'ble Mr. Justice K. VENKATARAMAN.
2. Most of the Engineering Colleges have submitted their proposal along with documents for revising / increasing the fees payable by the students.

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(3) **Fee Reviewed and fixed during 2019-2020 for 24 Colleges**

(a) Government Quota Non Accredidtated Courses Rs.50,000/-
Accredidated Courses Rs.55,000/-

(b) Management Quota Non Accredidated Courses Rs.1,20,000/-
Accredidated Courses Rs. 1,25,000/-

(c) Apart from the Above Fee:-

Rs.15,000/- Fixed for Placement and Training
Rs. 5,000/- Development Charges

8. Though based on the available materials, the fees has to be revised / increased, due to COVID -19 it has been felt by the Committee that the parents of the students will find it very difficult to pay the fees. However, reasonable increase of fees has to be made considering the facilities available in the Engineering Colleges and income and expenditure.
9. The following factors were considered by the Committee
   i. Principal adopted by Sri Krishna Committee
   ii. The expenditure details submitted by the colleges in proposals
   iii. Request submitted by the representatives of the Colleges regarding Skill Development and Placement Training expenditure
   iv. Seat sharing for admission of the students in Self Financing Engineering Colleges.
10. Keeping in mind all the above factors, which have been set above, the Committee is of the view, that the fee prescribed for the students admitted in the Government Quota need no change for the academic year 2020 -2021. However a sum of Rs. 20,000/- could be increased as tuition

Proceedings of the Fee committee (28.05.2019)

PROCEEDINGS OF THE COMMITTEE ON FIXATION OF FEE IN RESPECT OF SELF FINANCING PROFESSIONAL COLLEGES

PROC. NO.CFF/ UG - Engineering/ Fees/ 029 / 2019, Dated:28.05.2019

Hon'ble Mr. Justice N.V. Balasubramanian
Chairman

Thiru. Mangat Ram Sharma, IAS
Principal Secretary to Government
Higher Education Department
Member Secretary

Thiru. A. Kanagaraj
Member (Chartered Accountant of repute)

Shri. M. Sundresan
Member (representative of AICTE)

Prof. Dr. Esther Anlin Kala James,
Member
(Independent person of repute in the field of Education)

Thiru. K. Vivekanadan, I.A.S.,
Commissioner of Technical Education
Special Invitee

Ref:
1) G.O. Ms. No.226, Higher Education (J2) Department, dated 11-7-2007
2) Minute of the meeting held on dated.28.02.2019
3) Letter No. CFF / Fee / Engineering / 029 / 2019, dated: 07.03.2019
4) Minutes of the meeting held on dated.14.03.2019
5) Minutes of the meeting held on dated: 20.05.2019

ORDER:

1. With reference to G.O. 1st cited, the Committee was constituted for fixation of fee in respect of Self-Financing Professional Colleges under the Chairmanship of Hon'ble Mr. Justice N.V. Balasubramanian.

ORDER:

12. **The following maximum fee structure will take effect for the batch of the students admitted for the academic year 2019-2020 Only.**

Sl. No	Name of the Course		Fee fixed by the Committee Rs.	Remarks
1	B.E./B.Tech./B.Arch. (Category-I)	Non-Accredited Courses	**50,000 / Annum**	The fee is an all inclusive annual fee including various fees like Tuition fee, Admission fee, Special fee, Laboratory / Computer / Internet fee, Library fee, Sports fee, Maintenance and Amenities fee, Extracurricular activities fee and other recurring expenditure.
		Accredited Courses	**55,000 / Annum**	
2	B.E./B.Tech./B.Arch. (Category-II)	Non-Accredited Courses	**1,20,000 /Annum**	
		Accredited Courses	**1,25,000/ Annum**	

13. In addition to the above fees, the institution is permitted to collect an amount of Rs.5,000/- per student as development fee

14. In addition to the above recurring annual fee, the concerned Institutions are permitted to collect a refundable one time caution deposit not exceeding Rs.5, 000/- per student at the time of admission, to be refunded at the time the student leaving the institution.

8

Time schedule for the payment of Fee for the entire Programme

At the starting of each academic year and the schedule is flexible on student request.

No. of fee waivers granted with amount and name of the students: (2024-2025)

S.NO	REG.NO	NAME	CAT	D/H	YEAR	BRANCH	TOTAL
1	111521102053	HAREESH KANNA R	G	D	IV	CSE	38150
2	111521104028	DIKSHITHA R	M	D	IV	ECE	48600
3	111521104115	RACHITHA R	M	D	IV	ECE	48600
4	111521104122	RAJINI VYSHNAVI R	M	D	IV	ECE	17000
5	111521104142	SIVAKUMAR P	G	D	IV	ECE	48600
6	111521104143	SNEHA D	M	D	IV	ECE	48600
7	111521104175	VINOTH P	G	D	IV	ECE	25150
8	111521202024	JUDSON SMITH J	M	D	IV	CSBS	42150
9	111521202051	SHAKTHI PRIYA G	M	D	IV	CSBS	20000
10	111521203026	KEERTHIKA M N	M	D	IV	IT	48600
11	111522102116	PON MADHUMITHA A	G	D	III	CSE	20000
12	111522104010	AKSHAYA LAKSHMI M M (GOVT TUITIONH FEES	M	D	III	ECE	132150
13	111522104112	PRADEEP N M	G	D	III	ECE	20000
14	111522104148	SWATHI J	G	D	III	ECE	17000
15	111522202054	THANUSH S	M	D	III	CSBS	17000
16	111522203050	SHERLIN S	G	D	III	IT	48600
17	111523102029	BOOPATHI M	G	D	II	CSE	21150
18	111523102143	NISHITHA D	G	D	II	CSE	20000
19	111523102188	SENTHIL RAJ J	G	D	II	CSE	20000
20	111523104012	ARISH NARAYAN A	G	D	II	ECE	17000
21	111523104112	PIRATHISH R	G	H	II	ECE	50000
22	111523203006	ANUVARDHINI T	G	D	II	IT	48600
23	111523204035	OM NAREN D	M	D	II	AIML	20000
24	111524102139	NAREN KASINATHAN M K	G	D	I	CSE24	20000
25	111524104053	HARISH A	M	D	I	ECE24	82150
26	111524104062	INDUMATHI P	G	D	I	ECE24	72000
27	111524204001	AATHIL FELIX C	M	D	I	AIML	48600
28	111524204013	GATAMANENI SUDEEP	M	D	I	AIML	20000
						TOTAL	**1079700**

No. of scholarships offered by the institution, duration and amount (2024-2025)

SL. NO	NAME OF THE SCHOLARSHIP	NO. OF STUDENTS	TOTAL AMOUNT (Rs)	DURATION
1	First Graduate (FG)	250	62,50,000	One Year
2	BC/MBC	273	18,76,135	One Year
3	PMSS TFW SC/ST/SCC	99	53,05,000	One Year
4	GOVT. SCHOOL (7.5%)	57	70,42,248	One Year
5	AFW	38	10,27,500	One Year
6	MINORITY	18	1,25,82,500	One Year

Criteria for Fee waivers / Scholarship

- As per Government Norms

Estimated cost of Boarding and Lodging in Hostel: Rs 99000/-

10. Admission

No. of Seats sanctioned with the year of approval (2024-25)

Course	No. of seats sanctioned	Accreditation status	Period of accreditation	Letter No. and Date
B.E. Computer Science & Engineering	240	Accredited	2023-2024 to 2025-2026 i.e., upto 30.06.2026	F.No.33-243/2010-NBA Dated 16.02.2024
B.E. Electronics & Communication Engineering	180			
B.Tech. Information Technology	120			
B.Tech Computer Science and Business Systems	60	Not Eligible for Accreditation	-	-
B.Tech Artificial Intelligence and Machine Learning	60	Not Eligible for Accreditation	-	-

No. of Students admitted under various categories each year in the last three years.

Dept	Academic year 2024-25				Academic year 2023-24				Academic year 2022-23			
	Sanctioned	Admitted			Sanctioned	Admitted			Sanctioned	Admitted		
		CAT-I	CAT-II	Total		CAT-I	CAT-II	Total		CAT-I	CAT-II	Total
CSE	240	109	143	252	240	115	134	249	180	83	104	187
ECE	180	86	102	188	180	83	103	186	180	79	104	183
IT	120	58	64	122	60	28	35	63	60	29	34	63
CSBS	60	28	32	60	60	27	36	63	60	20	40	60
AIML	60	28	35	63	60	27	36	63	60	28	33	61
TOTAL	660	309	376	685	600	280	344	624	**540**	**239**	**315**	**554**

No. of application received during last two years for admission under Management Quota and number admitted.

Particular	Academic Year 2024-25		Academic Year 2023-24	
	Government	Management	Government	Management
No. of Application received	309	390	280	360
No. of students admitted	309	376	280	344

11. Admission Procedure

Admissions are made as per State government norms through Single Window Counselling and Consortium of Self-financing Engineering Colleges as per the norms of Tamil Nadu Government.

Entrance test / admission criteria - As per Tamil Nadu Government Norms

SELECTION OF STUDENTS

Admission to the Courses offered by the College is done under 2 categories:

1) Government Quota seats and

2) Management Quota seats.

Government Quota Admission

Seat sharing between these two categories is done based on the existing Government norms for the purpose of Admission of the students into Engineering and Technology courses in the Self-Financing

Colleges. R.M.D. Engineering College, being the Linguistic Minority Institution, is allowed to fill 50% of the total seats, branch wise, under the Management Quota and the other 50% of seats are surrendered to be filled under the Government Quota seats.

The Government Quota seats admission process is conducted and overseen by the Tamil Nadu Engineering Admissions (TNEA) through the Single Window Counseling system. Every year, the Tamil Nadu Engineering Admissions (TNEA) releases its Prospectus for public notice and calls for admission of students into B.E./B.TECH Courses under Government Quota seats. The admissions are done directly by the TNEA, and the selected candidates report to the College along with the Allotment Order issued by the TNEA for admission and based on the Allotment Order the College admits the students under Government Quota. The entire procedure followed by the TNEA including the Reservation Policy, Qualification and Eligibility Policy and the selection procedure is published in the Prospectus. TNEA prepares the Merit list of the students based on the marks obtained in the qualifying (PLUS 2) examinations for a cut off 200 and based on the cut off and choice of the students grants admission. The excerpts of the relevant pages from the TNEA Prospectus are attached herewith for ready reference.

A sample Allotment order of a candidate issued by the TNEA 2023 is attached herewith for your ready reference. The College being the most sought-after Engineering Institutions in the State, the students opting this College through Single Window Counseling of the TNEA are generally from the higher cut off Marks. The higher cut off marks, community wise, of the students admitted under Government Quota is attached which stand testimony to the Quality and merit of the students.

Management Quota Admission

Admission of the students into Engineering and Technology courses in the Self-Financing Colleges under the Management Quota seats are governed by the Committee constituted by the Government of Tamil Nadu. The Committee known as “COMMITTEE TO REGULATE-MONITOR THE ADMISSIONS OF STUDENTS TO PEROFESSIONAL COURSES BY SELF FINANCING PROFESSIONAL, ARTS AND SCIENCE COLLEGES” that regulates and monitors the admission of students made under:

1) Seats retained by the Institutions and the details of the students admitted under Management Quota Seats.

2) Details of students admitted under Government Quota lapsed seats.

R.M.D. Engineering College is a constituent member of the Consortium of Self-Financing Professional, Arts, and Science Colleges in Tamil Nadu. The consortium based on the orders and authorization issued by the “ COMMITTEE TO REGULATE-MONITOR THE ADMISSIONS OF STUDENTS TO PEROFESSIONAL COURSES BY SELF FINANCING PROFESSIONAL, ARTS AND SCIENCE COLLEGES” calls for applications from eligible candidates for admissions to Self-Financing Engineering Colleges and ranks the candidates based on the qualifying marks as in the TNEA Prospectus and issues allotment order based on the candidates’ choice of Institute.

Based on the allotment order and the consortium rank card, the college admits the candidates under Management Quota and submits the list of such admitted candidates to the COMMITTEE for scrutiny and approval.

Finally, the Directorate of Technical Education, Government of Tamil Nadu scrutinizes the list of students admitted; verifies the documents of qualification & Eligibility; reservation criteria and other relevant

original documents. And subsequently the Directorate of Technical Education, Government of Tamil Nadu issues Admission Approval order, with the list of students, to the Colleges.

The College being the most sought-after Engineering Institutions in the State, the students Ranked and selected under Consortium Ranking are generally from the higher cut off Marks. The Ranks of the students admitted under Management Quota is attached which stand testimony to the Quality and merit of the students.

12.Criteria and Weightage for Admission

- As per Government Norms

13.List of Applicants

After the admission process, the Directorate of Technical Education, Government of Tamil Nadu scrutinizes the list of students admitted; verifies the documents of qualification & Eligibility; reservation criteria and other relevant original documents.

And subsequently the Directorate of Technical Education, Government of Tamil Nadu issues Admission Approval order, with the list of students, to the Colleges. The list of approved students of 2023-2024 is attached.

14.Results of Admission under Management Quota/ Vacant seats

Admission of the students into Engineering and Technology courses in the Self-Financing Colleges under the Management Quota seats are governed by the Committee constituted by the Government of Tamil Nadu. The Committee known as “COMMITTEE TO REGULATE-MONITOR THE ADMISSIONS OF STUDENTS TO PROFESSIONAL COURSES BY SELF FINANCING PROFESSIONAL, ARTS AND SCIENCE COLLEGES” that regulates and monitors the admission of students made under:

1) Seats retained by the Institutions and the details of the students admitted under Management Quota Seats.

2) Details of students admitted under Government Quota lapsed seats.

15.Information of Infrastructure and other Resources available

Number of Classrooms and size of each.

Sl.No.	Name of the Block	Area (Length * Width) in sq.m.	Number of Rooms	Type of roof	Capacity
1	CS BLOCK	66	31	Permanent	1860
2	AIML BLOCK	66	10	Permanent	600
3	NM BLOCK	66	12	Permanent	720
4	ECE BLOCK	66	10	Permanent	600
5	MAIN BLOCK	66	10	Permanent	600

Number of Tutorial Rooms and size.

- 16 Rooms each with size of 33 sq.mtr

umber of Laboratories and size of each

SL.No	Degree & Course	Laboratory/Workshop/ Studio	Name of the Laboratory	Area of the Laboratory required(sq.m.)	Area of the Laboratory available(sq.m.)	Deficiency %
1	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS511 NETWORKS LABORATORY	66	66	0
2	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS411 INTERNET PROGRAMMING LAB	66	66	0
3	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS412 OPERATING SYSTEMS LABORATORY	66	66	0
4	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22MA401 PROBABILITY AND STATISTICS INTEGRATED LABORATORY	66	66	0
5	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22cs201 data structures	66	66	0
6	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21EC441 MICROPROCESSORS AND INTERFACING LAB INTEGRATED	66	66	0
7	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22MA101 MATRICES AND CALCULUS INTEGRATED LABORATORY	66	66	0
8	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS311 OBJECT ORIENTED PROGRAMMING LABORATORY	66	66	0
9	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22PH201 PHYSICS FOR COMPUTER SCIENCE AND INFORMATION TECHNOLOGY	66	66	0
10	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21IT412 DATABASE MANAGEMENT SYSTEMS LABORATORY	66	66	0
11	B.E.-Computer Science and Engineering	Engg. & Tech. Workshop	22GE112 PRODUCT DEVELOPMENT LAB I	200	200	0
12	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22cs102 software development practices	66	66	0
13	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS301 DIGITAL PRINCIPLES AND SYSTEM DESIGN LAB INTEGRATED	66	66	0
14	B.E.-Computer Science and Engineering	Engg. & Tech. Workshop	22GE211 PRODUCT DEVELOPMENT LAB II	200	200	0
15	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22ec101 digital principles and system design integrated laboratory	66	66	0
16	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22GE111 COMPUTER AIDED ENGINEERING GRAPHICS LAB INTEGRATED	66	66	0

17	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22cs101 problem solving using c plus plus	66	66	0
18	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22HS101 PROFESSIONAL COMMUNICATION	66	66	0
19	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	DATABASE MANAGEMENT SYSTEM LABORATORY	66	66	0
20	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	digital principles and system design laboratory	66	66	0
21	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22cS202 JAVA PROGRAMMING	66	66	0
22	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	PHYSICS FOR COMPUTER SCIENCE AND INFORMATION TECHNOLOGY	66	66	0
23	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22it201 database management systems	66	66	0
24	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22MA101 MATRICES AND CALCULUS INTEGRATED LABORATORY	66	66	0
25	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	ENGINEERING CHEMISTRY INTEGRATED LABORATORY	66	66	0
26	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS301 ADVANCED JAVA PROGRAMMING LAB INTEGRATED	66	66	0
27	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22cs101 problem solving using c plus plus	66	66	0
28	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS303 DESIGN AND ANALYSIS OF ALGORITHMS LAB INTEGRATED	66	66	0
29	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22cs201 data structures lab integrated	66	66	0
30	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS304 OPERATING SYSTEMS LAB INTEGRATED	66	66	0
31	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
32	B.E.-Computer Science and Engineering	Engg. & Tech. Workshop	22GE311 PRODUCT DEVELOPMENT LAB III	200	200	0
33	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22MA401 PROBABILITY AND STATISTICS LAB INTEGRATED	66	66	0
34	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22AM301 ARTIFICIAL INTELLIGENCE LAB INTEGRATED	66	66	0

35	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS401 DISTRIBUTED AND CLOUD COMPUTING LAB INTEGRATED	66	66	0
36	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS402 WEB DEVELOPMENT FRAMEWORKS LAB INTEGRATED	66	66	0
37	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	CS8711 CLOUD COMPUTING LABORATORY	66	66	0
38	B.E.-Computer Science and Engineering	Engg. & Tech. Workshop	22GE411 PRODUCT DEVELOPMENT LAB IV	200	200	0
39	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS501 COMPUTER NETWORKS LAB INTEGRATED	66	66	0
40	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	IT8761 SECURITY LABORATORY	66	66	0
41	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS502 THEORY OF COMPUTATION LAB INTEGRATED	66	66	0
42	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS503 MACHINE LEARNING LAB INTEGRATED	66	66	0
43	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS612 SECURITY LABORATORY	66	66	0
44	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS611 MOBILE APPLICATION DEVELOPMENT LABORATORY	66	66	0
45	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS601 COMPILER DESIGN LAB INTEGRATED	66	66	0
46	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22CS602 OBJECT ORIENTED SOFTWARE ENGINEERING LAB INTEGRATED	66	66	0
47	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS703 DATA ANALYTICS LAB INTEGRATED	66	66	0
48	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	C PROGRAMMING LABORATORY	66	66	0
49	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	DATA STRUCTURES LABORATORY	66	66	0
50	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATIO N	66	66	0
51	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	PYTHON PROGRAMMING INTEGRATED LAB	66	66	0
52	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0

53	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22ph201 physics for computer science and information technology integrated laboratory	66	66	0
54	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS502 OBJECT ORIENTED ANALYSIS AND DESIGN LAB INTEGRATED	66	66	0
55	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS601 COMPILER DESIGN LAB INTEGRATED	66	66	0
56	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	22MA201 TRANSFORMAND NUMERICAL METHODS INTEGRATED LABORATORY	66	66	0
57	B.E.-Computer Science and Engineering	Engg. & Tech. Laboratory	21CS512 ARTIFICIAL INTELLIGENCE LABORATORY	66	66	0
58	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22MA101 MATRICES AND CALCULUS INTEGRATED LABORATORY	66	66	0
59	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	digital principles and system design laboratory	66	66	0
60	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATIO N	66	66	0
61	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8563 COMMUNICATION NETWORKS LABORATORY	66	66	0
62	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22CS202 JAVA PROGRAMMING	66	66	0
63	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22CS202 JAVA PROGRAMMING	66	66	0
64	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC311 ANALOG AND DIGITAL CIRCUITS LABORATORY	66	66	0
65	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8761 ADVANCED COMMUNICATION LABORATORY	66	66	0
66	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	PHYSICS FOR ELECTRONICS ENGINEERING	66	66	0
67	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC412 LINEAR INTEGRATED CIRCUITS LABORATORY	66	66	0
68	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	ENGINEERING CHEMISTRY INTEGRATED LABORATORY	66	66	0
69	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21CS614 ADVANCED APTITUDE AND CODING SKILLS II	66	66	0

70	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22MA302 STATISTICS AND LINEAR ALGEBRA INTEGRATED LABORATORY	66	66	0
71	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21CS512 ADVANCED APTITUDE CODING SKILLS I	66	66	0
72	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC612 VLSI DESIGN LABORATORY	66	66	0
73	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC611 EMBEDDED SYSTEMS AND RTOS LABORATORY	66	66	0
74	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22MA401 PROBABILITY AND RANDOM PROCESSES	66	66	0
75	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC513 FOUNDATION LAB ON MACHINE LEARNING	66	66	0
76	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22ph101 physics for electronics engineering	66	66	0
77	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC512 COMMUNICATION SYSTEMS LABORATORY	66	66	0
78	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC511 COMMUNICATIO N NETWORKS AND DSP LABORATORY	66	66	0
79	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC412 LINEAR INTEGRATED CIRCUITS LABORATORY	66	66	0
80	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC101 DIGITAL PRINCIPLES AND SYSTEM DESIGN INTEGRATED LABORATORY	66	66	0
81	B.E.-Electronics and Communication Engineering	Engg. & Tech. Workshop	22GE112 PRODUCT DEVELOPMENT LAB I	200	200	0
82	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21CS313 APTITUDE AND CODING SKILLS I	66	66	0
83	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8711 EMBEDDED SYSTEMS LABORATORY	66	66	0
84	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8681 MICROPROCESSO R AND MICROCONTROLL ER LABORATORY	66	66	0
85	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC201 ELECTRON DEVICES AND CIRCUIT THEORY INTEGRATED LABORATORY	66	66	0
86	B.E.-Electronics and Communication Engineering	Engg. & Tech. Workshop	22GE211 PRODUCT DEVELOPMENT LAB II	200	200	0

87	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22cs102 software development practices	66	66	0
88	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	C PROGRAMMING LABORATORY	66	66	0
89	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC612 COURSE BASED PROJECT II	66	66	0
90	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22MA201 TRANSFORM AND NUMERICAL METHODS INTEGRATED LABORATORY	66	66	0
91	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22GE111 COMPUTER AIDED ENGINEERING GRAPHICS LAB INTEGRATED	66	66	0
92	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	EC8711 EMBEDDED LABORATORY	66	66	0
93	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC611 DIGITAL SIGNAL PROCESSING LABORATORY	66	66	0
94	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC603 EMBEDDED SYSTEMS LAB INTEGRATED	66	66	0
95	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22cs201 data structures lab integrated	66	66	0
96	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	Data structures laboratory	66	66	0
97	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC512 COURSE BASED PROJECT I	66	66	0
98	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC511 COMMUNICATION SYSTEMS LABORATORY	66	66	0
99	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	21EC503 VLSI DESIGN LAB INTEGRATED	66	66	0
100	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC301 SIGNALS AND SYSTEMS LAB INTEGRATED	66	66	0
101	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC302 ANALOG ELECTRONICS LAB INTEGRATED	66	66	0
102	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22CS305 PROBLEM SOLVING AND PYTHON PROGRAMMING	66	66	0
103	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC311 PRODUCT DEVELOPMENT LAB 3	66	66	0
104	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC401 CONTROL ENGINEERING LAB INTEGRATED	66	66	0

105	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC402 LINEAR INTEGRATED CIRCUITS LAB INTEGRATED	66	66	0
106	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC403 ANALOG AND DIGITAL COMMUNICATION LAB INTEGRATED	66	66	0
107	B.E.-Electronics and Communication Engineering	Engg. & Tech. Laboratory	22EC411 PRODUCT DEVELOPMENT LAB 4	66	66	0
108	B.E.- General Engineering	Engg. & Tech. Workshop	PRODUCT DEVELOPMENT LAB 1	200	200	0
109	B.E.- General Engineering	Engg. & Tech. Laboratory	data structures laboratory	66	66	0
110	B.E.- General Engineering	Engg. & Tech. Laboratory	COMPUTER AIDED ENGINEERING GRAPHICS	66	66	0
111	B.E.- General Engineering	Engg. & Tech. Laboratory	physics laboratory	66	66	0
112	B.E.- General Engineering	Engg. & Tech. Laboratory	C Programming laboratory	66	66	0
113	B.E.- General Engineering	Engg. & Tech. Workshop	PRODUCT DEVELOPMENT LAB 2	200	200	0
114	B.E.- General Engineering	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
115	B.E.- General Engineering	Engg. & Tech. Laboratory	physics laboratory	66	66	0
116	B.E.- General Engineering	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
117	B.Tech.-Information Technology	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
118	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21CS211 DATA STRUCTURE S LABORATOR Y	66	66	0
119	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21CS202 PYTHON PROGRAMMING LABORATORY INTEGRATED COURSE	66	66	0
120	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT311 OBJECT ORIENTED PROGRAMMING PRINCIPLES LABORATORY	66	66	0
121	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21CS412 OPERATING SYSTEMS LABORATORY	66	66	0
122	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21CS301 DIGITAL PRINCIPLES AND SYSTEM DESIGN LABORATORY INTEGRATED COURSE	66	66	0

123	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT401 SOFTWARE ENGINEERING LABORATORY INTEGRATED COURSE	66	66	0
124	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT411 WEB TECHNOLOGY LABORATORY	66	66	0
125	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT412 DATABASE MANAGEMENT SYSTEMS LABORATORY	66	66	0
126	B.Tech.-Information Technology	Engg. & Tech. Laboratory	IT8761 SECURITY LABORATORY	66	66	0
127	B.Tech.-Information Technology	Engg. & Tech. Laboratory	IT8711 FOSS AND CLOUD COMPUTING LABORATORY	66	66	0
128	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21CS611 MOBILE APPLICATION DEVELOPMENT LABORATORY	66	66	0
129	B.Tech.-Information Technology	Engg. & Tech. Laboratory	C PROGRAMMING LABORATORY	66	66	0
130	B.Tech.-Information Technology	Engg. & Tech. Laboratory	DATA STRUCTURES LABORATORY	66	66	0
131	B.Tech.-Information Technology	Engg. & Tech. Laboratory	PYTHON PROGRAMMING INTEGRATED LAB	66	66	0
132	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21GE111 C PROGRAMMING LABORATORY	66	66	0
133	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22cs102 software development practices	66	66	0
134	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22CS202 JAVA PROGRAMMING	66	66	0
135	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22it201 database management systems	66	66	0
136	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22MA101 MATRICES AND CALCULUS INTEGRATD LABAORATORY	66	66	0
137	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22cs101 problem solving using c plus plus	66	66	0
138	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22cs201 data structures lab integrated	66	66	0
139	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22CS301 ADVANCED JAVA PROGRAMMING LAB INTEGRATED	66	66	0
140	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22CS304 OPERATING SYSTEMS LAB INTEGRATED	66	66	0
141	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22MA401 PROBABILITY AND STATISTICS LAB INTEGRATED	66	66	0

142	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22IT401 ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING LAB INTEGRATED	66	66	0
143	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22IT402 COMPUTER ARCHITECTURE AND MICROPROCESSORS LAB INTEGRATED	66	66	0
144	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22IT403 WEB DEVELOPMENT FRAMEWORKS LAB INTEGRATED	66	66	0
145	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22IT404 APPLICATION SYSTEM DESIGN WITH UML LAB INTEGRATED	66	66	0
146	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22IT903 SOFTWARE TESTING AND AUTOMATION LAB INTEGRATED	66	66	0
147	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT911 DEVOPS LAB INTEGRATED	66	66	0
148	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT931 MICROSERVICE ARCHITECTURE LAB INTEGRATED	66	66	0
149	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22CS303 DESIGN AND ANALYSIS OF ALGORITHMS LAB INTEGRATED	66	66	0
150	B.Tech.-Information Technology	Engg. & Tech. Workshop	22GE311 PRODUCT DEVELOPMENT LAB III	200	200	0
151	B.Tech.-Information Technology	Engg. & Tech. Workshop	22GE411 PRODUCT DEVELOPMENT LAB IV	200	200	0
152	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22MA101 MATRICES AND CALCULUS INTEGRATED LABORATORY	66	66	0
153	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22MA201 TRANSFORM AND NUMERICAL METHODS INTEGRATED LABORATORY	66	66	0
154	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22MA401 PROBABILITY AND STATISTICS INTEGRATED LABORATORY	66	66	0
155	B.Tech.-Information Technology	Engg. & Tech. Workshop	22GE112 PRODUCT DEVELOPMENT LAB I	200	200	0
156	B.Tech.-Information Technology	Engg. & Tech. Workshop	22GE211 PRODUCT DEVELOPMENT LAB II	200	200	0
157	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22GE111 COMPUTER AIDED ENGINEERING GRAPHICS LAB INTERGRATED	66	66	0

158	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22EC101 DIGITAL PRINCIPLES AND SYSTEM DESIGN INTEGRATED LABORATORY	66	66	0
159	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22HS101 PROFESSIONAL COMMUNICATION	66	66	0
160	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT501 WEB TECHNOLOGY FRAMEWORK	66	66	0
161	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21EC441 MICROPROCESSOR AND INTERFACING	66	66	0
162	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21CS511 NETWORKS LABORATORY	66	66	0
163	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT512 BIG DATA ANALYTICS LABORATORY	66	66	0
164	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT602 ARTIFICIAL INTELLIGENCE AND MACHINE LEARNING	66	66	0
165	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT603 CYBER SECURITY	66	66	0
166	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21CS711 CLOUD COMPUTING LABORATORY	66	66	0
167	B.Tech.-Information Technology	Engg. & Tech. Laboratory	21IT511 OBJECT ORIENTED SYSTEM DESIGN LABORATORY	66	66	0
168	B.Tech.-Information Technology	Engg. & Tech. Laboratory	ENGINEERING CHEMISTRY INTEGRATED LABORATORY	66	66	0
169	B.Tech.-Information Technology	Engg. & Tech. Laboratory	PHYSICS FOR COMPUTER SCIENCE AND INFORMATION TECHNOLOGY	66	66	0
170	B.Tech.-Information Technology	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATION	66	66	0
171	B.Tech.-Information Technology	Engg. & Tech. Laboratory	digital principles and system design laboratory	66	66	0
172	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22cs101 problem solving using c plus plus	66	66	0
173	B.Tech.-Information Technology	Engg. & Tech. Laboratory	22cs201 data structures	66	66	0
174	B.Tech.-Information Technology	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
175	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22ma406 operations research	66	66	0
176	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	Fundamentals of computer science integrated laboratory	66	66	0

177	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	66	66	0
178	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY	66	66	0
179	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CS8461 OPERATING SYSTEMS LABORATORY	66	66	0
180	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8411 COMPUTATIONAL STATISTICS LABORATORY	66	66	0
181	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8611 BUSINESS ANALYTICS LABORATORY DESIGN ARCHITECTURE END TO END	66	66	0
182	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8612 ARTIFICIAL INTELLIGENCE LABORATORY	66	66	0
183	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8711 INFORMATION SECURITY LABORATORY	66	66	0
184	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	IT8511 WEB TECHNOLOGY LABORATORY	66	66	0
185	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8712 MINI PROJECT	66	66	0
186	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8811 PROJECT WORK	66	66	0
187	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21MA203 STATISTICAL METHODS LAB INTEGRATED	66	66	0
188	B.Tech.-Computer Science and Business	Engg. & Tech. Laboratory	21CB201 DATA STRUCTURES AND ALGORITHMS LAB INTEGRATED	66	66	0

	Systems					
189	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21EC241 PRINCIPLES OF ELECTRONICS ENGINEERING LAB INTEGRATED	66	66	0
190	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB303 OBJECT ORIENTED PROGRAMMING LAB INTEGRATED	66	66	0
191	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21MA304 COMPUTATIONAL STATISTICS LAB INTEGRATED	66	66	0
192	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB304 SOFTWARE ENGINEERING LAB INTEGRATED	66	66	0
193	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB401 OPERATING SYSTEMS LAB UNIX INTEGRATED	66	66	0

194	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB402 DATABASE MANAGEMENT SYSTEMS LAB INTEGRATED	66	66	0
195	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB403 SOFTWARE DESIGN WITH UML LAB INTEGRATED	66	66	0
196	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB405 OPERATIONAL RESEARCH LAB INTEGRATED	66	66	0
197	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB502 COMPILER DESIGN LAB INTEGRATED	66	66	0
198	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB902 CLOUD MICRO SERVICES AND APPLICATION LAB INTEGRATED	66	66	0
199	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB903 MACHINE LEARNING LAB INTEGRATED	66	66	0
200	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB601 COMPUTER NETWORKS LAB INTEGRATED	66	66	0
201	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB602 INFORMATION SECURITY LAB INTEGRATED	66	66	0
202	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB603 ARTIFICIAL INTELLIGENCE LAB INTEGRATED	66	66	0
203	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB908 MODERN WEB APPLICATIONS LAB INTEGRATED	66	66	0
204	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21CB909 DATA MINING AND ANALYTICS LAB INTEGRATED	66	66	0
205	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	FUNDAMENTALS OF PHYSICS	66	66	0
206	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	COMPUTATIONAL STATISTICS LABORATORY	66	66	0
207	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22CS202 JAVA PROGRAMMING	66	66	0
208	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	BUSINESS COMMUNICATION AND VALUE SCIENCES ii	66	66	0
209	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATION	66	66	0

210	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22CS102 SOFTWARE DEVELOPMENT PRACTICES	66	66	0
211	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22EC202 PRINCIPLES OF ELECTRONICS ENGINEERING LABORATORY INTEGRATED COURSE	66	66	0

212	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22CS102 SOFTWARE DEVELOPMENT PRACTICES	66	66	0
213	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	fundamentals of physics lab	66	66	0
214	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cs101 problem solving using c plus plus	66	66	0
215	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	BUSINESS COMMUNICATIO N AND VALUE SCIENCES I	66	66	0
216	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	OPERATING SYSTEMS LAB	66	66	0
217	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22HS101 PROFESSIONAL COMMUNICATION	66	66	0
218	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Workshop	22GE211 PRODUCT DEVELOPMENT LAB II	200	200	0
219	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22EC202 PRINCIPLES OF ELECTRONICS ENGINEERING INTEGRATED LABORATORY	66	66	0
220	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22ge101 principles of electrical engineering integrated laboratory	66	66	0
221	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cb201 data structures and algorithms	66	66	0
222	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Workshop	22GE112 PRODUCT DEVELOPMENT LAB I	200	200	0
223	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22PH102 FUNDAMENTALS OF PHYSICS	66	66	0
224	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22MA404 OPERATIONS RESEARCH INTEGRATED LABORATORY	66	66	0
225	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22MA304 COMPUTATIONAL STATISTICS INTEGRATED LABORATORY	66	66	0
226	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22MA203 STATISTICAL METHODS INTEGRATED LABORATORY	66	66	0

227	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cb201 data structures and algorithms	66	66	0
228	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cb201 data structures and algorithms	66	66	0
229	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cs101 problem solving using c plus plus	66	66	0

230	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	daTA STRUCTURES AND ALGORITHMS INTEGRATED LABORATORY	66	66	0
231	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cb404 design and analysis of algorithms lab integrated	66	66	0
232	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cb403 operating systems	66	66	0
233	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22it403 web development framework lab integrated	66	66	0
234	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cb304 database management systems lab integrated	66	66	0
235	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22cb303 software design with uml	66	66	0
236	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	22ma304 computational statistics	66	66	0
237	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21cb811 project work	66	66	0
238	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21cb702 it workshop scilab lab integrated	66	66	0
239	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21cb701 usability design of software application lab integrated	66	66	0
240	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8511 MINI PROJECT SOFTWARE SYSTEM DESIGN ARCHITECTURE END TO END	66	66	0
241	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21cb922 business intelligence and analytics	66	66	0
242	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	21cb921 python for data analytics	66	66	0
243	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	CW8512 SOFT SKILLS LABORATORY	66	66	0
244	B.Tech.-Computer Science and Business Systems	Engg. & Tech. Laboratory	AD8261 DATA STRUCTURE DESIGN LABORATORY	66	66	0
245	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22HS101 PROFESSIONAL COMMUNICATION	66	66	0

246	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	PROFESSIONAL COMMUNICATIO N	66	66	0
247	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22CS102 SOFTWARE DEVELOPMENT AND PRACTICES	66	66	0
248	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22CS102 SOFTWARE DEVELOPMENT AND PRACTICES	66	66	0

249	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	ENGINEERING CHEMISTRY INTEGRATED LABORATORY	66	66	0
250	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22MA101 MATRICES AND CALCULUS INTEGRATED LABORATORY	66	66	0
251	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
252	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22PH201 PHYSICS FOR COMPUTER SCIENCE AND INFORMATION TECHNOLOGY	66	66	0
253	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	digital principles and system design laboratory	66	66	0
254	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22MA401 PROBABILITY AND STATISTICS INTEGRATED LABORATORY	66	66	0
255	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22EC101 DIGITAL PRINCIPLES AND SYSTEM DESIGN INTEGRATED LABORATORY	66	66	0
256	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22GE111 COMPUTER AIDED ENGINEERING GRAPHICS LAB INTEGRATED	66	66	0
257	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Workshop	22GE211 PRODUCT DEVELOPMENT LAB II	200	200	0
258	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22MA201 TRANSFORM AND NUMERICAL METHODS INTEGRATED LABORATORY	66	66	0
259	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22MA101 MATRICES AND CALCULUS INTEGRATED LABORATORY	66	66	0
260	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22cs101 problem solving using c plus plus	66	66	0
261	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Workshop	22ge112 product development lab i	200	200	0
262	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22cs201 data structures lab integrated	66	66	0
263	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21IT411 DATABASE MANAGEMENT SYSTEMS LABORATORY	66	66	0
264	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21CS311 OBJECT ORIENTED PROGRAMMING LABORATORY	66	66	0

265	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21AM502 DATA VISUALIZATION LAB INTEGRATED	66	66	0
266	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21AM511 NEURAL NETWORKS LABORATORY	66	66	0
267	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	MINI PROJECT AND DESIGN THINKING PRACTICES LABORATORY	66	66	0
268	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21AM601 PRINCIPLES AND PRACTICES IN DEEP LEARNING LAB INTEGRATED	66	66	0
269	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21am302 principles of artificial intelligence lab integrated	66	66	0
270	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21am411 machine learning essentials laboratory	66	66	0
271	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21am412 data analytics laboratory	66	66	0
272	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	21am404 operating systems fundamentals lab integrated	66	66	0
273	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22cs101 problem solving using c plus plus	66	66	0
274	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
275	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22it201 database management systems	66	66	0
276	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	PYTHON PROGRAMMING INTEGRATED LAB	66	66	0
277	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22CS202 JAVA PROGRAMMING	66	66	0
278	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22cs201 data structures	66	66	0
279	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	PHYSICS FOR COMPUTER SCIENCE AND INFORMATION TECHNOLOGY	66	66	0
280	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22HS101 PROFESSIONAL COMMUNICATION	66	66	0
281	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	22CS102 SOFTWARE DEVELOPMENT AND PRACTICES	66	66	0
282	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	C programming laboratory	66	66	0
283	B.Tech.-Artificial Intelligence and Machine Learning	Engg. & Tech. Laboratory	DATA STRUCTURES LABORATORY	66	66	0

284	B.Tech.-General Engineering	Engg. & Tech. Laboratory	ADVANCED READING AND WRITING LABORATORY	66	66	0
285	B.Tech.-General Engineering	Engg. & Tech. Workshop	PRODUCT DEVELOPMENT LAB 1	200	200	0
286	B.Tech.-General Engineering	Engg. & Tech. Laboratory	physics laboratory	66	66	0
287	B.Tech.-General Engineering	Engg. & Tech. Workshop	PRODUCT DEVELOPMENT LAB 2	200	200	0
288	B.Tech.-General Engineering	Engg. & Tech. Laboratory	INTERPERSONAL SKILLS LISTENING AND SPEAKING LABORATORY	66	66	0
289	B.Tech.-General Engineering	Engg. & Tech. Laboratory	COMPUTER AIDED ENGINEERING LABORATORY	66	66	0

Number of Drawing Halls with capacity of each.

Degree	Number Available	Area of each drawing hall required (sq.m.)	Area of the drawing hall available (sq.m.)
B.E.	03	264	396

Number of Computer Centre with capacity of each.

Sl.No	Name of the Computer Centre	No. of Systems
1	BAY-1	66
2	BAY-2	66
3	BAY-3	68
4	BAY-4	68
5	BAY-5	66
6	BAY-6	66
7	BAY-7	30
8	BAY-8	66
9	BAY-9	84
10	BAY-10	100
TOTAL		**680**

Computing Facilities

Internet Bandwidth

The Computer Centre is connected to leased line internet connection of **1000 Mbps** (**500 Mbps** leased line internet connection from **AIRTEL Data Services** and **500 Mbps** leased line internet connection from **TATA Tele Business Services**). The Computer Centre provides internet services to all the systems in the Computer Centre and all the departments in campus. The Computer Centre provides Wi-Fi facility to all the departments in campus and provides 24 Hrs Wi-Fi facility to all the Hostel students.

Number and Configuration of Systems

Number of Systems available in Computer Centre : 680

Apple Mac : 30 Nos

Intel i9 Processor : 145 Nos

Intel i7 Processor : 485 Nos

Intel i5 Processor : 20 Nos

Dell Power Edge T620 Server: 01

Dell Power Edge T420 Server: 02

All the Systems in the Computer Centre are connected by LAN and WAN

Sl.No.	Course Type	Total Student	Number of Terminals On LAN / WAN
1	B.E.	1560	480
2	B.Tech.	780	200

Major software packages available

1. Microsoft License & Software Products

- Microsoft® Win Server Standard Core All Languages License & Software Assurance Open Value 2 Licenses Level E 1 Year Academic AP
- Microsoft® SQL Server Standard All Languages License & Software Assurance Open Value Level E 1 Year Academic AP
- Microsoft® SQL CAL All Languages License & Software Assurance Open Value Level E 1 Year Academic Enterprise Device CAL
- Microsoft® Visual Studio Professional MSDN All Languages License & Software Assurance Open Value Level E 1 Year Academic AP
- Microsoft® Desktop Education All Languages License & Software Assurance Open Value Level E 1 Year Academic Enterprise

2. Windows Operating System

Windows 11, Windows 10

3. Linux Operating System

Ubuntu OS

4. Fortinet Firewall

Application Software

1. MS-Office Professional plus with Core CAL
2. Visual Studio Pro
3. SQL Client Access License
4. Rational Rose Software
5. Java Development Kit(JDK 1.5)
6. Turbo C and C ++ / GCC and G++ for Ubuntu
7. My-SQL
8. iOS – X Code
9. Android for Mobile App Development
10. NetBeans or Eclipse
11. Hi Class software Ver 4.2 / Business Management skill / Manage Stress Focus
12. OPNET / NS2 Simulator
13. LEX / YACC Tool
14. KF Sensor Tool / Tensor Flow / Knime Tool / Net Stumbler / Open Nebula Tool / Open Stack
15. Python and R-Tool
16. Apache / Tomcat Server

17. WAMP / XAMP

18. GnuPG / Snort / N-Stalker

19. Virtual box / Openstack / Hadoop / Coludism / GAE launcher

20. Argo UML / Eclipse IDE

Central Examination Facility, Number of rooms and Capacity of each

40 (Each 66 Sqmt) Examination halls are available to accommodate 1000 students at a time at the rate of 25 students per hall other than the 65 labs for the practical Examinations

Online examination facility (Number of Nodes, Internet band width, etc.)

- Adequate facilities are available for the conduct of online examinations.

Sl.No	Name of the Computer Centre	No. of Systems
1	BAY-1	66
2	BAY-2	66
3	BAY-3	68
4	BAY-4	68
5	BAY-5	66
6	BAY-6	66
7	BAY-7	30
8	BAY-8	66
9	BAY-9	84
10	BAY-10	100
TOTAL		**680**

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Barrier Free Built Environment for disabled and elderly persons

- YES

Fire and Safety Certificate

FIRE SERVICE LICENSE

(Under Section 13 of the Tamil Nadu Fire Service Act 1985 and with Tamil Nadu Fire Service Rules 1990 Appendix III)

L.DIS.No.TN-3520240510113/B/2024 **Date: 15-05-2024**

LICENSE is here by granted under section 13 of the Tamil Nadu Fire Service Act 1985, for **To Run Educational Institution** in the name of **M/s. R.M.D Engineering College,** within the jurisdiction of **Kavaraipettai** at **R.S.M Nagar, Kavaraipettai, Gummidipoondi Taluk, Thiruvallur District** With total building area of **35574.60 Sq.mt** with Subject to the condition mentioned below noted there on and such other conditions as may be prescribed. The above premises inspected by **Station Officer, Ponneri** on 14-05-2024

CONDITIONS

As per Tamil Nadu Fire Service Act 1985 Section 13 of Chapter II and appendix V of this Act.

1. According to G.O. No.713 Home (Police-17) Department, Dated 17.08.2005 the Permanent employees & Safety employees who is responsible for the safety of working in the company should be trained.
2. This License is valid for **One Year** from the date of issue.
3. Regular License has to be obtained from the Competent Authority.
4. The Following Fire & Life safety Systems / Arrangements provided in the building should be kept in good working condition always.
    a) Portable First Aid Fire Extinguishers – 78 Nos,
    b) Fire Bucket with sand – 06 Nos
    c) First Aid Box – 06 Nos
    d) Over Head Water Tank with Capacity of 50000 ltr and Borewell.
5. As per NBC 2016 The Following Fire & Life Safety Systems / Arrangements should be provided within 6 months.
    a) First Aid Hose Reel System with 450 LPM Pump should be Provided.
6. If there is any deviation from the Government Rule and Act the LICENSE issued will Stand Cancelled.
7. Mock Drill should be conducted once in 6 months along with nearby Fire & Rescue Service Stations.

(Office seal with date)

15 MAY 2024

15/5/2024

DISTRICT OFFICER,
FIRE & RESCUE SERVICES,
THIRUVALLUR DISTRICT,
THIRUVALLUR.

To:

M/s. R.M.D Engineering College,
R.S.M Nagar, Kavaraipettai,
Gummidipoondi Taluk,
Thiruvallur District.

Copy to:

Deputy Director, Fire & Rescue Service, North western Region, Vellore-1.

Hostel Facilities

Boys hostel details: (As per the academic Year 2024-25)

Block Name	Total Admitted Strength	Carpet area of room (sq.m.)	Number of rooms Available	Room capacity Available
BOYS HOSTEL I	362	9	366	3294
BOYS HOSTEL II	186	20	65	1300

Girls hostel details:(As per the academic Year 2024-2025)

Block Name	Total Admitted Strength	Carpet area of room (sq.m.)	Number of rooms Available	Room capacity Available
GIRLS HOSTEL I	119	20	50	1000
GIRLS HOSTEL 2	270	09	200	2700

Number of Library books/ebooks/Titles/Journals available

The college central library is in the new block with a carpet area of 1065 sq.mt. It contains 62309 volumes of books and 15014 titles. The Library subscribes to 36 national Journals, 849 leading international E-journals and 10662 E-books in Delnet database, 41 Magazines, 16 News Papers, 1828 back volumes of journals and 5528 CD ROMs. Books are classified and arranged according to the Universal Decimal classification scheme. The value of books, periodicals and other materials in the library is around Rs.2.7 Crore

Number of Library books/Titles/ Journals available (program wise)

S.No.	Course(s)	Number of Titles	Number of Volumes	Journals
				National
01	Science and Humanities	3096	11581	06
02	Computer Science and Engineering	3678	14818	06
03	Electronics and Communication Engineering	4968	22954	06
04	Information Technology	2498	10973	06
05	Computer Science and Business Systems	366	1004	06
06	Artificial Intelligence and Machine Learning	408	979	06

E-LIBRARY FACILTY

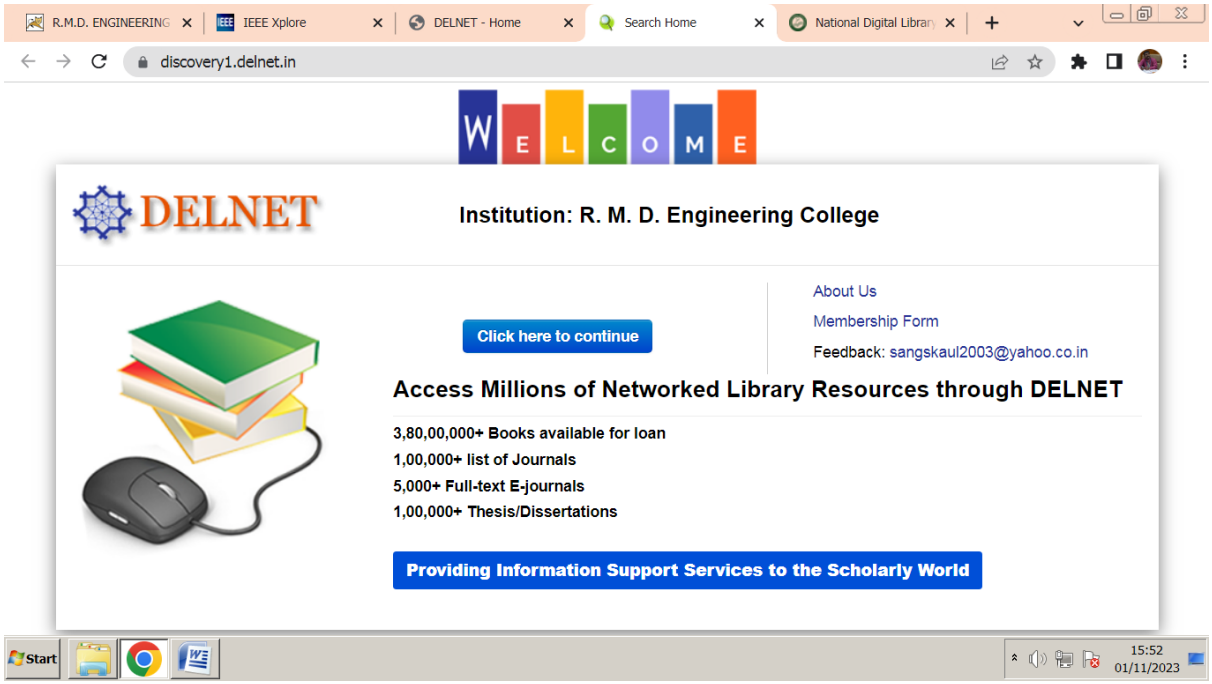
List of online National / International E-Journal subscribed

- IETE-Journals
- IEI-Journals
- Delnet (Developing Library Network)

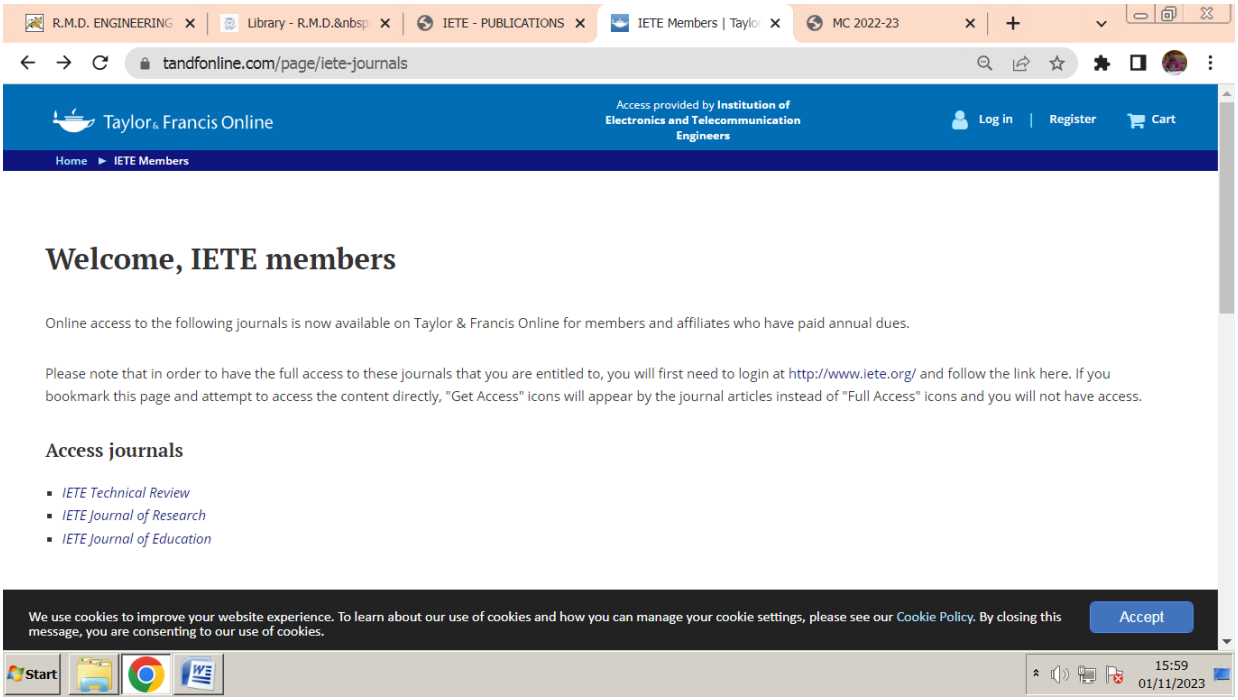
PUBLISHERS	DEPARTMENT						
	CSE	IT	ECE	CSBS	AIML	S/H	TOTAL
DELNET - Journals	154	59	35	55	73	465	762
IEI-Journals	01	-	02	-	01	01	05
IETE-Journals	01	01	-	-	-	01	02
TOTAL	**156**	**60**	**37**	**55**	**74**	**467**	**849**

DETAILS OF DIGITAL LIBRARY

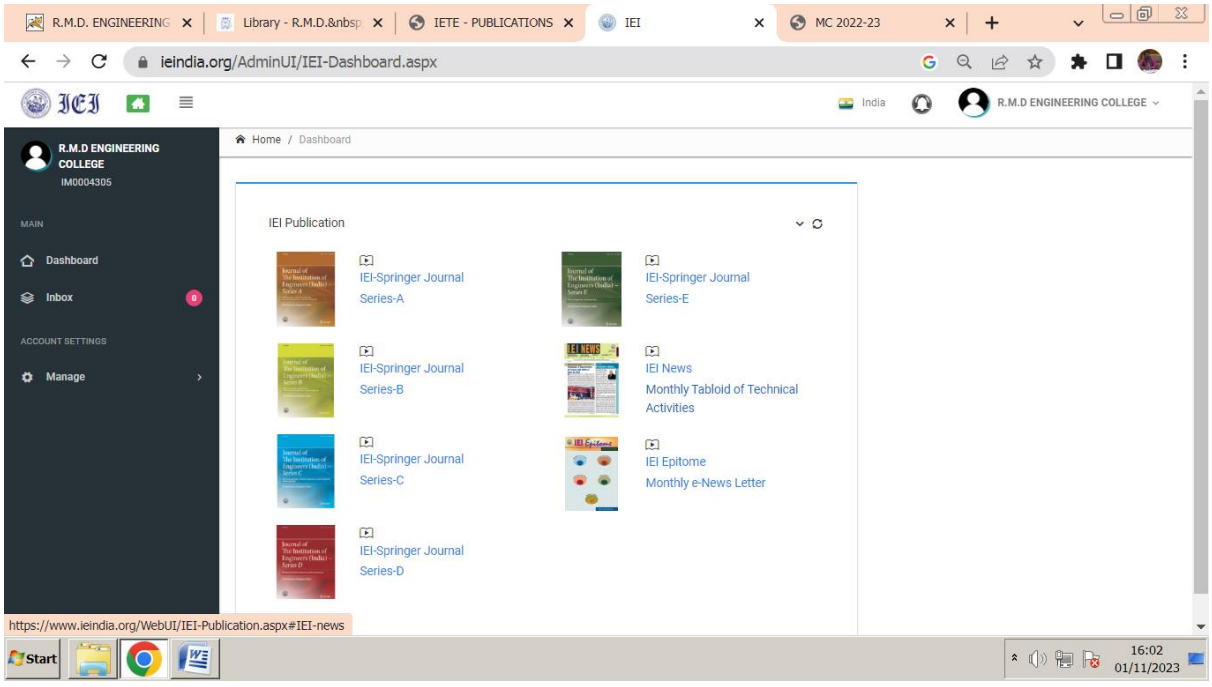
DEVELOPING LIBRARY NETWORK DATABASE(E-JOURNALS & E-BOOKS)



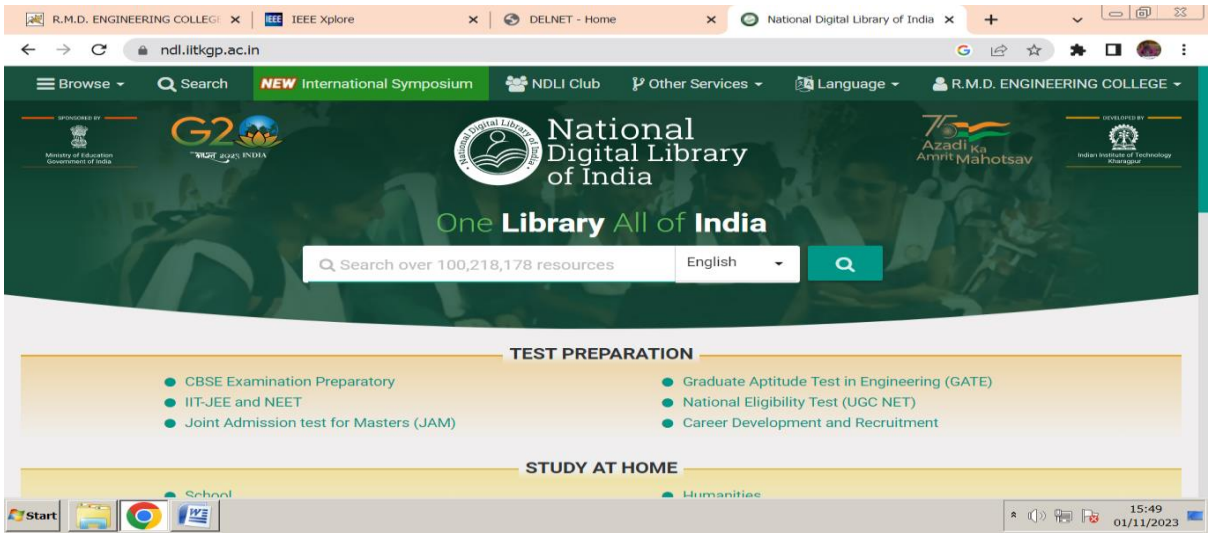
INSTITUTIONAL MEMEBRSHIP OF IETE ONLINE JOURNALS



INSTITUTIONAL MEMEBRSHIP OF IEI ONLINE JOURNALS

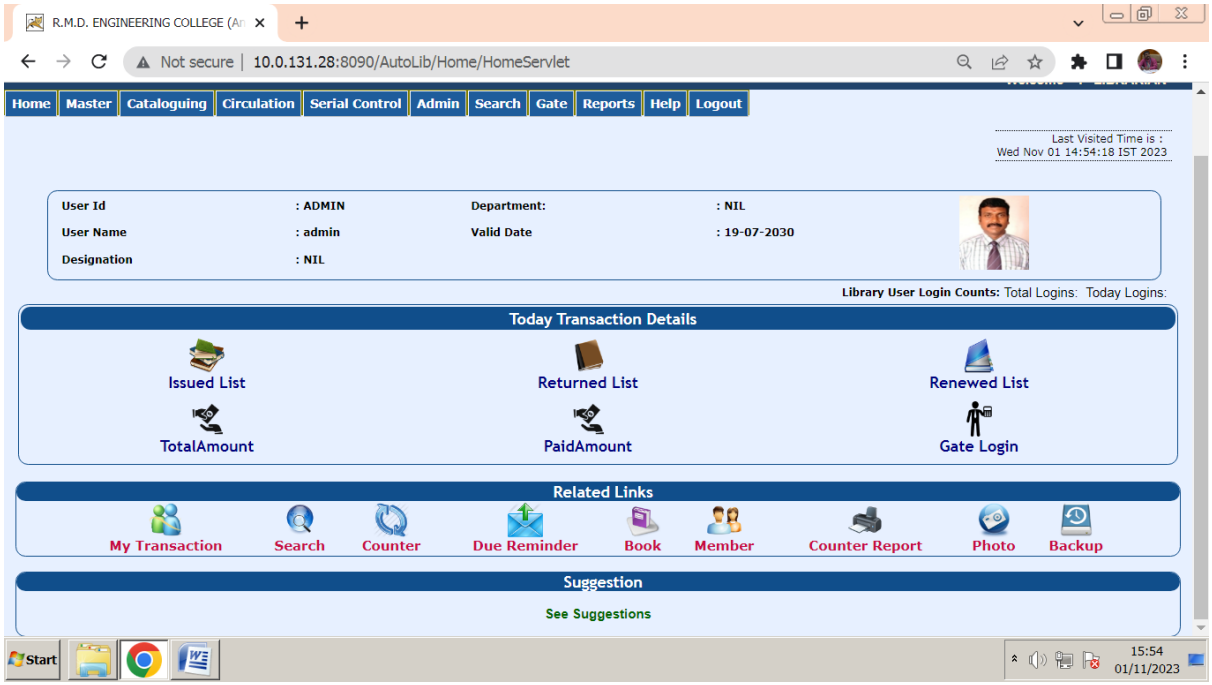


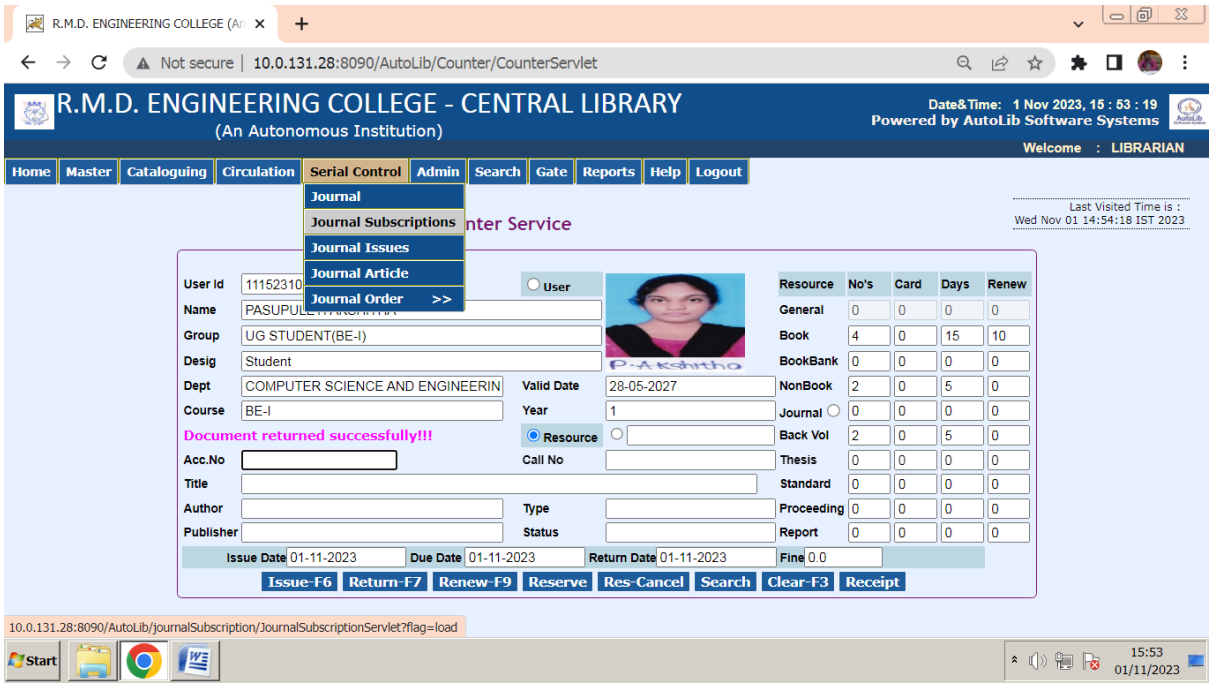
INSTITUTIONAL MEMEBRSHIP OF NDLI DATABASE

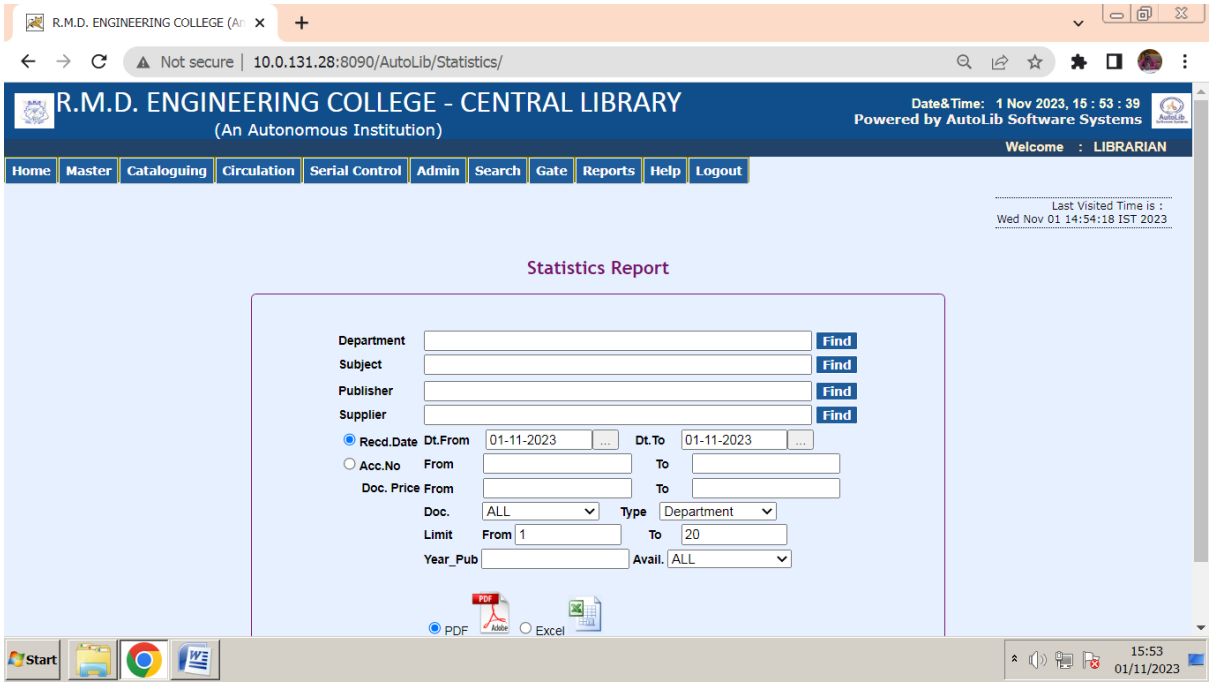


INSTITUTIONAL MEMEBRSHIP OF NDLI CLUB CERTIFICATE

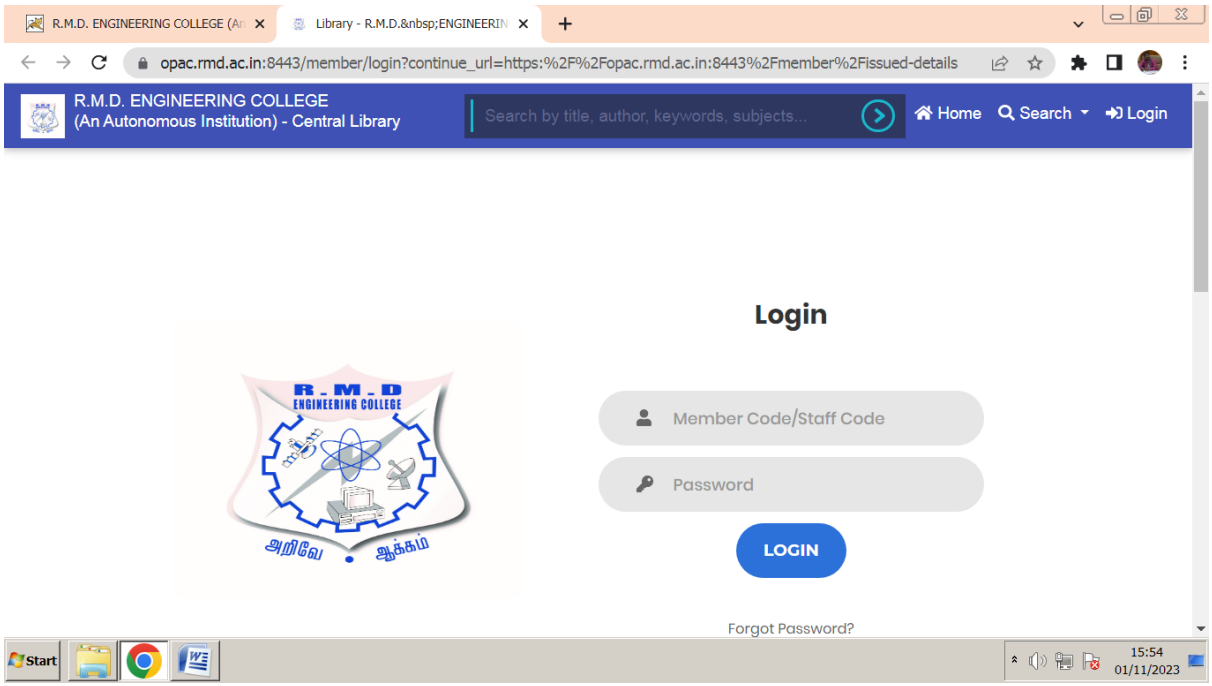
CENTRAL LIBRARY WEB BASED LIBRARY SOFTWARE



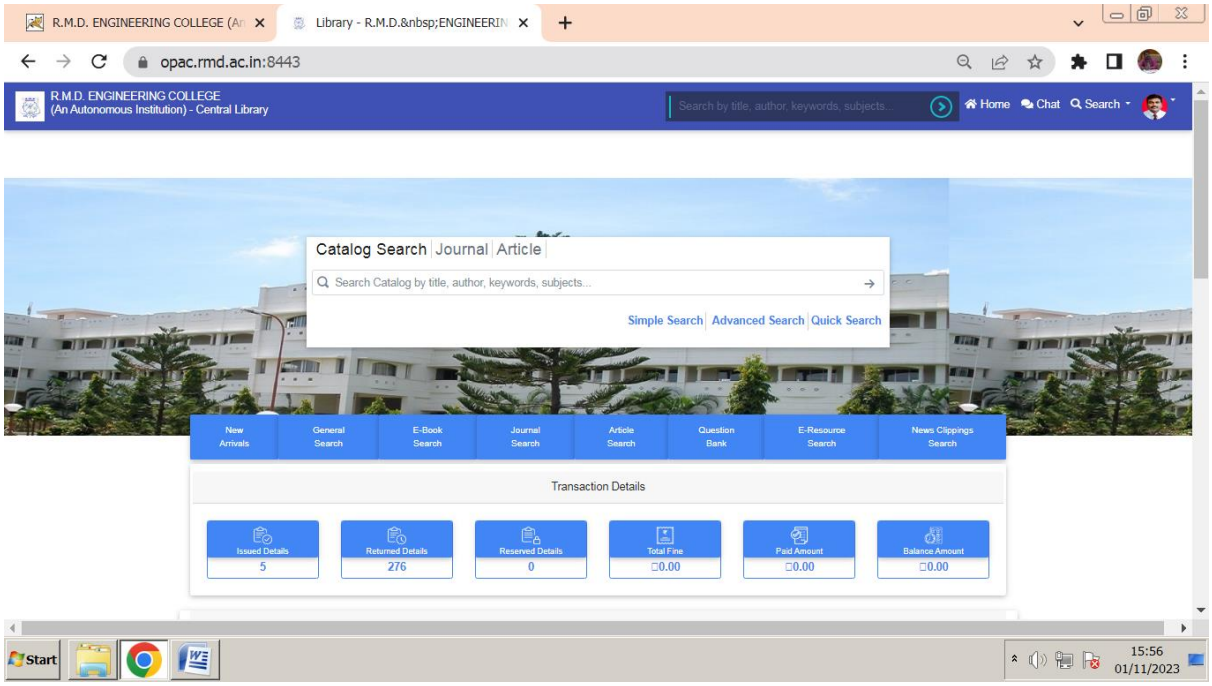




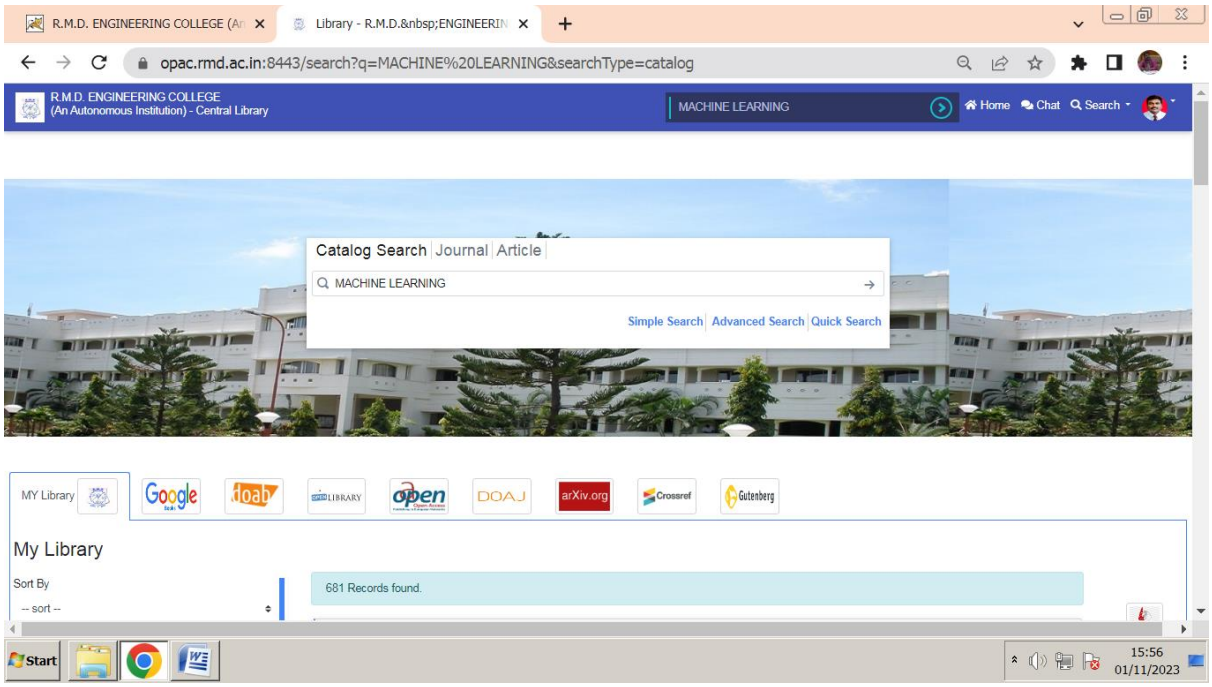
CENTRAL LIBRARY WEB BASED ONLINE PUBLIC ACCESS CATALOGUE



RESOURCE SEARCH ENGINE OF ONLINE PUBLIC ACCESS CATALOGUE

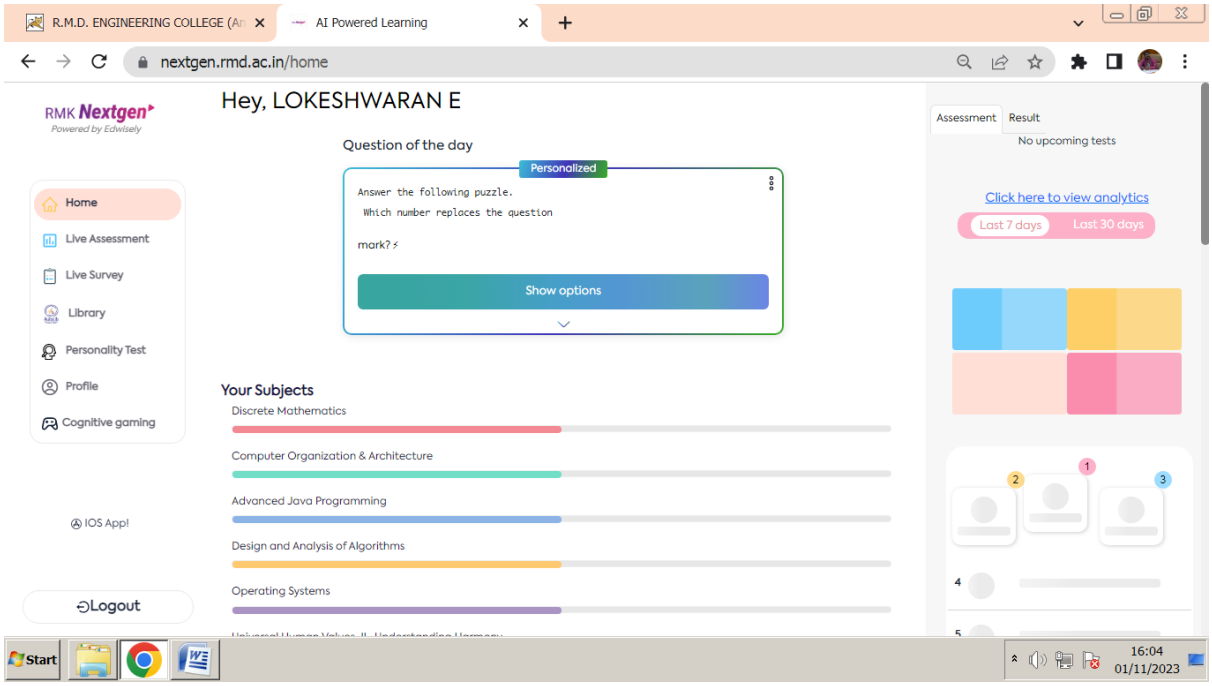


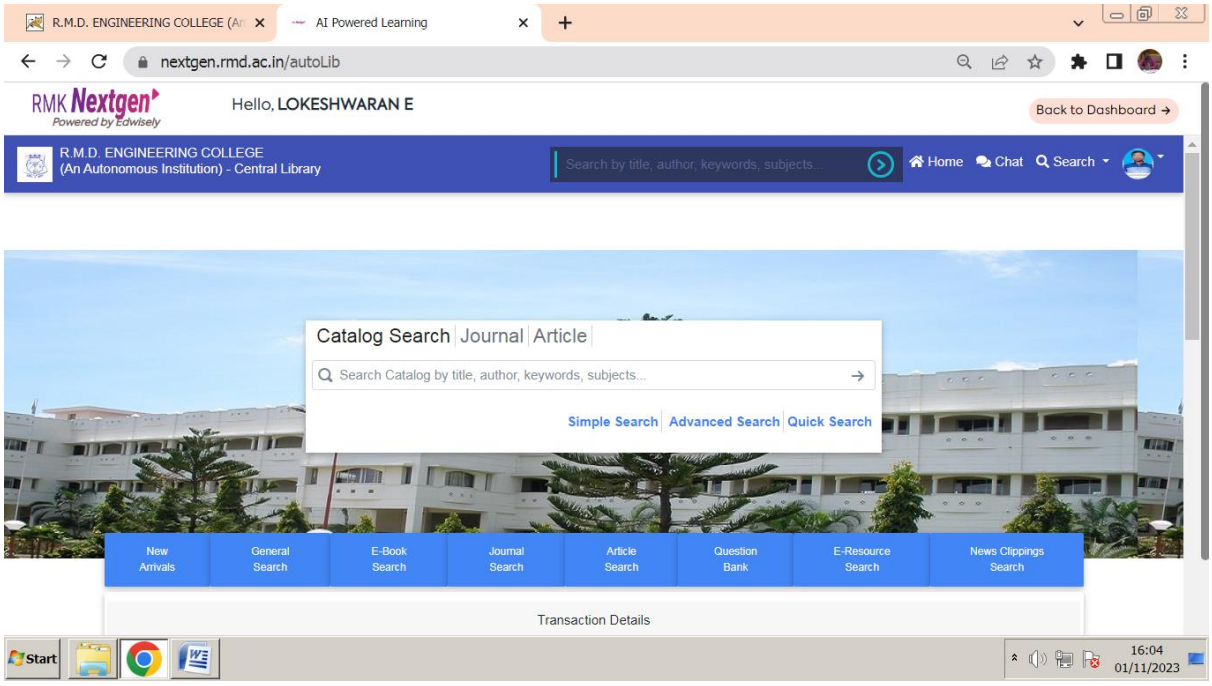
SEARCH ENGINE OF ON CAMPUS & OFF CAMPUS RESOURCES



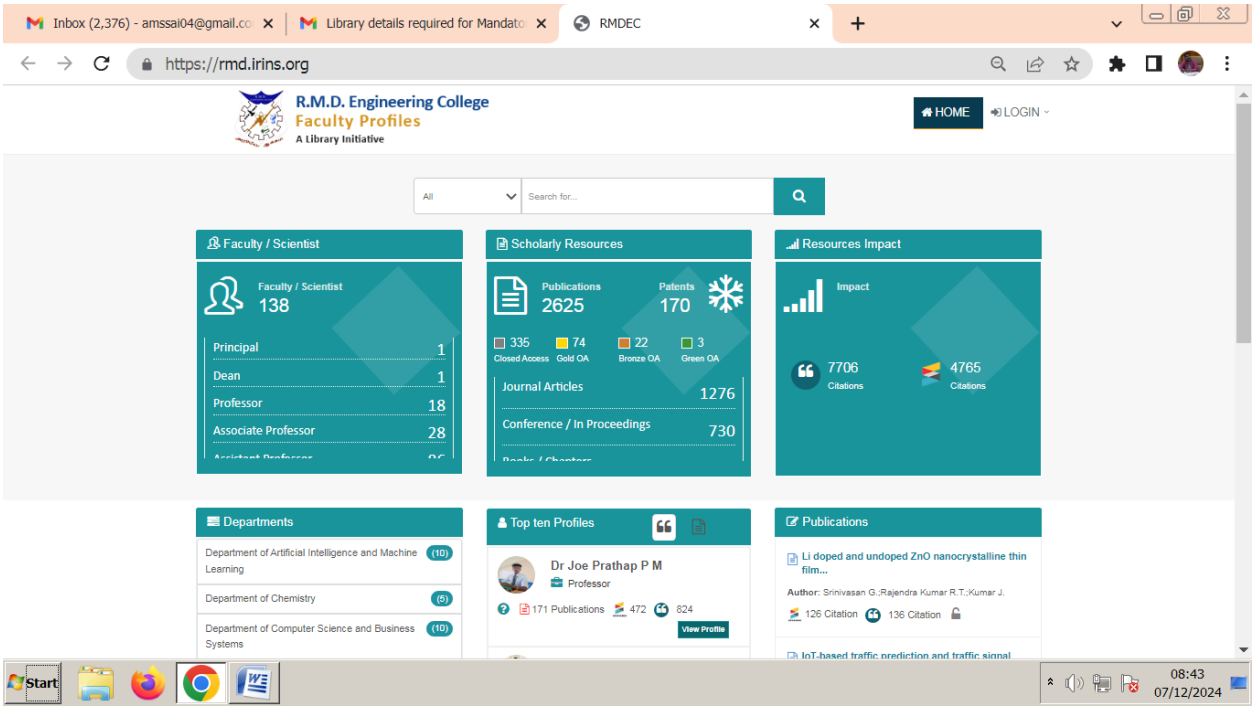
REMOTE ACCESS OF ONLINE PUBLIC ACCESS CATALOGUE

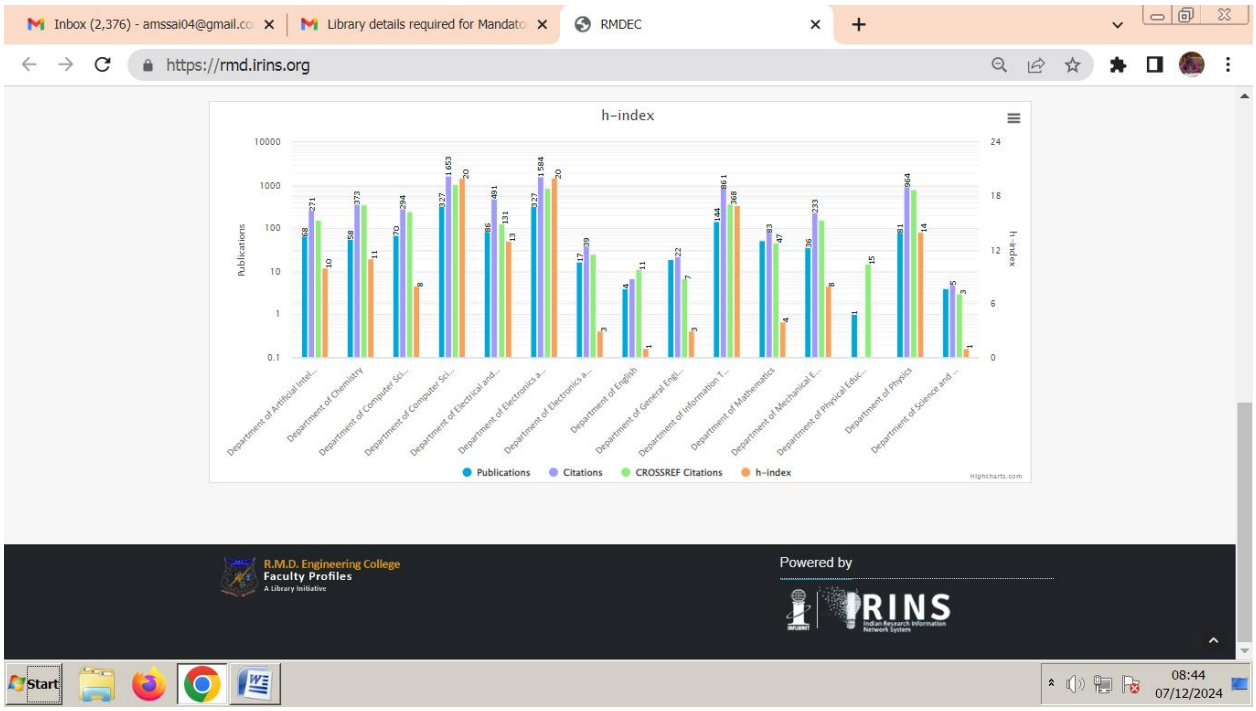
THROUGH RMK NEXTGEN MOBILE APP





VIDWAN/IRINS PORTAL (INFLIBNET) FACULTY PROFILES DATABASE





List of Major Equipment/Facilities in each Laboratory/Workshop

Requirements for a batch of 30 students

Computer Science and Engineering:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	CS8261 C PROGRAMMING LABORATORY	Systems with Linux Operating System with gnu compiler	30	30
2	CS8381 DATA STRUCTURES LABORATORY	Systems with Linux Operating System with gnu compiler	30	30
3	CS8382 DIGITAL SYSTEM LABORATORY	Digital trainer kits	30	30
		Digital ICs	30	30
		Software: HDL simulator	30	30
4	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Netbeans or Eclipse	30	30
5	CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY	Systems with MySql	30	30
		Visual Studio	30	30
		Server	1	1
6	CS8461 OPERATING SYSTEMS LABORATORY	Systems with Linux OS and GNU Computer	30	30
7	CS8581 NETWORKS LABORATORY	Standalone Desktops	30	30
		C / C++ / Java / Python / Equivalent Compiler Network Simulator like NS2 / Glomosim / OPNEt / Packet Tracer / Equivalent	30	30
8	EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY	8086 Microprocessor trainer kit with power supply	15	15
		8051 Microcontroller trainer kit	15	15
		Traffic light control interfacing card compatible with 8086 & 8051 kits	5	5
		Stepper motor control interfacing compatible with 8086 & 8051kits	5	5
		Digital clock interfacing board compatible with 8086 & 8051 kits	5	5
		Keyboard & Display interface board compatible with 8086 & 8051 kits	5	5
		Printer interfacing card compatible with 8086 & 8051 kits	5	5
		A/D and D/A interfacing card compatible with 8086 & 8051 kits	5	5
		Serial and Parallel interfacing card compatible with 8086 & 8051 kits	5	5
9	CS8582 OBJECT ORIENTED ANALYSIS AND	Rational Suite (User License)	30	30

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
	DESIGN LABORATORY	Open-Source Alternatives: ArgoUML,StarUML, Visual Paradigm (or) Equivalent Eclipse IDE and Junit	30	30
		PCs	30	30
10	CS8661 INTERNET PROGRAMMING LABORATORY	Systems	30	30
		Server (Web Server)	1	1
		Java/JSP/ISP Webserver/Apache Tomcat / MySQL / Dreamweaver or Equivalent, WAMP/XAMP	30	30
11	CS8662 MOBILE APPLICATION DEVELOPMENT LABORATORY	Standalone desktops with Windows or Android or iOS or Equivalent Mobile Application Development Tools with appropriate emulators and debuggers Tools with appropriate emulators and debuggers	30	30
12	IT8761 SECURITY LABORATORY	C / C++ / Java or equivalent compiler GnuPG, Snort, N-Stalker or Equivalent	30	30
		PCs	30	30
13	CS8711 CLOUD COMPUTING LABORATORY	Virtual box, VMware Workstation, Cloud Environment Creation, Openstack, Hadoop, Coludism, GAE Launcher	30	30

Electrical and Electronics Engineering:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	EE8261 ELECTRIC CIRCUITS LABORATORY	RegulatedPowerSupply:0- 15V D.C	10	10
		Function Generator(1MHz)	10	10
		Single Phase Energy Meter	1	1
		Oscilloscope(20MHz).	10	10
		Digital Storage Oscilloscope(20MHz)	1	1
		PC With Circuit Simulation Software (10 Users)	10	10
		e-Sim/Scilab/Pspice / Matlab /otherEquivalentsoftwarePackage)	10	10
		Printer	1	1
		AC/DC-Voltmeters	10	10
		Ammeters	10	10
		Multi-meters	10	10
		Single Phase Watt meter	3	3

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Decade Resistance Box, Decade Inductance Box, Decade Capacitance Box (Each)	6	6
		Circuit Connection Boards	10	10
2	EC8311 ELECTRONICS LABORATORY	Semiconductor devices like Diode, ZenerDiode, NPN Transistors, JFET, UJT, Photo diode, Photo Transistor	10	10
		Resistors, Capacitors and inductors	10	10
		Necessary digitalIC8	10	10
		Function Generators	10	10
		Regulated 3 output Power Supply 5+_ 15V	10	10
		CRO	10	10
		Storage Oscilloscope	1	1
		Breadboards	10	10
3	EE8461 LINEAR AND DIGITAL INTEGRATED CIRCUITS LABORATORY	DC Shunt Motor with Loading Arrangement	3	3
		DC Shunt Motor Coupled With Three phase Alternator	1	1
		Single Phase Transformer	4	4
		DC Series Motor with Loading Arrangement	1	1
		DC Compound motor with loading arrangement	1	1
		Three Phase Induction Motor with Loading Arrangement	2	2
		Single Phase Induction Motor with Loading Arrangement	1	1
		DC Shunt Motor Coupled With DC Compound Generator	2	2
		DC Shunt Motor Coupled With DC Shunt Generator	1	1
		Tachometer-Digital/Analog	8	8
		Single Phase Auto Transformer	2	2
		Three Phase Auto Transformer	1	1
		Single Phase Resistive Loading Bank	2	2
		Three Phase Resistive Loading Bank	2	2
4	EE8461 LINEAR AND DIGITAL INTEGRATED CIRCUITS LABORATORY	Dual,(0-30V)variable Power Supply	10	10
		CRO(30MHz)	9	9
		Digital Multimeter	10	10
		Function Generator(1MHz)	8	8
		IC Tester(Analog)	2	2

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Breadboard	10	10
		Computer(PSPICE installed)	1	1
		IC741/ICNE555/566/565	10	10
		Digital IC types	10	10
		LED	10	10
		LM317	10	10
		LM723	10	10
		ICSG3524/ SG3525	10	10
		Transistor-2N3391	10	10
		Diodes,IN4001, BY126	10	10
		Zener diodes	10	10
		Potentiometer	10	10
		Step-downtransformer230V/12-0-12V	10	10
		Capacitor	10	10
		Resistors1/4WattAssorted	10	10
		Single Strand Wire	10	10
5	EE8411 ELECTRICAL MACHINES LABORATORY II	SynchronousInductionmotor3HP	1	1
		DC Shunt Motor Coupled With Three phase Alternator	4	4
		DC Shunt Motor Coupled With Three phase Slipring Induction motor	1	1
		Three Phase Induction Motor with Loading Arrangement	2	2
		Single Phase Induction Motor with Loading Arrangement	2	2
		Tachometer-Digital/Analog	8	8
		Single Phase Auto Transformer	2	2
		Three Phase Auto Transformer	3	3
		Single Phase Resistive Loading Bank	2	2
		Three Phase Resistive Loading Bank	2	2
		Capacitor Bank	1	1
6	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Netbeans or Eclipse	30	30
7	EE8511 CONTROL AND	PID controller simulation and learner kit	1	1

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
	INSTRUMENTATION LABORATORY	DSO for capturing transience	1	1
		Personal computers with contro lsystem Simulation packages	10	10
		DC motor- Generator test set-up for Evaluation of motor parameters	1	1
		CRO 30MHz	1	1
		2MHzFunctionGenerator	1	1
		Position Control Systems Kit(with manual)	1	1
		Tacho Generator Coupling set	1	1
		AC Synchro transmitter&receiver	1	1
		Digital multimeters, speed and torque sensors	10	10
		R,L,CBridgekit (withmanual)	1	1
		Electric heater	1	1
		Thermometer	1	1
		Thermistor(silicontype)RTDnickeltype	1	1
		30 psi Pressure chamber(complete set)	1	1
		Current generator(0-20mA)	1	1
		Air foot pump(with necessary connecting tubes)	1	1
		LVDT20mmcorelengthmovabletype	1	1
		CRO 30MHz	1	1
		Optical sensor	1	1
		Strain Gauge Kit with Handy lever beam	1	1
		100gmweights	10	10
		Flow measurement Trainer kit (1/2 HP Motor,Water tank,Digital Milli ammeter,complete set)	1	1
		Single phase Auto transformer	1	1
		Watt hour meter(energymeter)	1	1
		Voltmeter Rheostat Stopwatch Connecting wires	20	20
		ICtrainerkit	1	1
		InstrumentationAmplifierkit	1	1
		Analog-DigitalandDigital-Analog converters(ADCandDACs)	1	1
8	EE8661 POWER ELECTRONICS AND	Device characteristics (for SCR,MOSFET,TRIAC,GTO,IGCTandIGBTkitwith builtin/discretepowersupplyandmeters)	2	2

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
	DRIVES LABORATORY	Single phase SCR based half controlled on verter and fullycontrolled converter along with built-in/separate/firingcircuit/moduleandmeter	2	2
		MOSFET based stepup and step down choppers(Builtin/ Discrete)	1	1
		IGBTbased single phase PWM inverter module/Discrete Component	2	2
		IGBTbased three phase PWM inverter module/Discrete Component	2	2
		Switched mode power converte rmodule/Discrete Component	2	2
		SCR &TRIAC based1 phaseAC controller along with lampor rheostatload	2	2
		Cyclo converter kit with firing module	1	1
		Dual regulated Dc power supply with common ground	5	5
		Cathode ray Oscilloscope	10	10
		IsolationTransformer	5	5
		Single phaseAutotransformer	3	3
		Components (Inductance,Capacitance)	3	3
		Multimeter	5	5
		LCRmeter	3	3
		Rheostats of various ranges	2	2
		Worktables	10	10
		DC and AC meters of required ranges	20	20
9	EE8681 MICROPROCESSORS AND MICROCONTROLLERS LABORATORY	8085 MicroprocessorTrainer with Power Supply	15	15
		8051MicroController Trainer Kit with power supply	15	15
		8255 Interface board	5	5
		8251Interface board	5	5
		8259Interface board	5	5
		8279 Keyboard/Display Interface board	5	5
		8254 timer counter	5	5
		ADC and DAC card	5	5
		AC & DC motor with Controller	5	5

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Traffic Light Control System	5	5
10	EE8711 POWER SYSTEM SIMULATION LABORATORY	Personal computers (Intel i3, 80GB,2GBRAM)	30	30
		Printerlaser	1	1
		Dotmatrix	1	1
		Server (Intel i5, 80GB, 2GBRAM) (HighSpeedProcessor)	1	1
		powersystem simulations software	5	5
		Compliers:C, C++,VB,VC++	30	30
11	EE8712 RENEWABLE ENERGY SYSTEMS LABORATORY	Personal computers (Intel i3, 80GB,2GBRAM)	15	15
		CRO 30MHz	9	9
		Digital Multimeter	10	10
		PV panels-100W,24V	1	1
		Battery storage system with charge and Discharge contro l40Ah	1	1
		PV Emulator	1	1
		Micro Wind Energy Generatormodule	1	1
		Potentiometer	5	5
		Step-downtransformer230V/12-0-12V	5	5

Electronics and Communication Engineering:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	EC8261 CIRCUITS AND DEVICES LABORATORY	BC107,BC148,2N2646,BFW10	25	25
		IN4007,Zenerdiodes	25	25
		Resistors,Capacitors,Inductors-	100	100
		Bread Boards	15	15
		CRO(30MHz)	15	15
		Function Generators(3MHz)	10	10
		Dual Regulate dpower Supplies(0-30V)	10	10
2	EC8361 ANALOG AND DIGITAL CIRCUITS LABORATORY	CRO(30MHz)	15	15
		Signal Generator /Function Generators(3 MHz)	15	15
		Dual Regulated Power Supplies (0-30V)	15	15
		Standalone desktop PCs with SPICEsoftware	15	15
		Transistor/FET(BJT-NPN-PNPandNMOS/PMOS)	50	50

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Dual power supply/single mode powersupply	15	15
		Resistors,Capacitors, Inductors	50	50
		Diodes,Zenerdiode	10	10
		ICTrainer Kit	15	15
		Bread Boards	15	15
		ComputerwithHDLsoftware	15	15
		Sevensegmentdisplay	15	15
		Multimeter	15	15
		ICs7400/7402/ 7404/ 7486/ 7408/ 7432/7483/74150/ 74151/74147/ 7445/7476/7491/555/7494 /7447/ 74180/7485/7473/74138 /7411/ 7474	50	50
3	EC8381 FUNDAMENTALS OF DATA STRUCTURES IN C LABORATORY	Standalone desktops (or) Server supporting with C compiler	30	**30**
4	EC8461 CIRCUITS DESIGN AND SIMULATION LABORATORY	CRO(Min30MHz)	15	15
		Signal Generator /Function Generators(2 MHz)	15	15
		Dua lRegulated Power Supplies (0-30V)	15	15
		Digital Multimeter	15	15
		Digital LCR Meter	2	2
		Standalone desktops PC	15	15
		Transistor/FET(BJT-NPN-PNPandNMOS/PMOS)	50	50
		Transistors, Resistors, Capacitors, Inductors,diodes,ZenerDiodes,BreadBoards,Transformers	50	50
		SPICE Circuit Simulation Software (any public domain or commercial software)	15	15
5	EC8462 LINEAR INTEGRATED CIRCUITS LABORATORY	CRO /DSO(Min30MHz)	15	15
		Signal Generator /Function Generators (2 MHz)	15	15
		Dual Regulated Power Supplies (0-30V)	15	15
		DigitalMultimeter	15	15
		ICtester	5	5
		Standalone desktops PC	15	15
		Transistors, Resistors, Capacitors,diodes, Zener diodes, Bread Boards,Transformers, wires, Power		

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		transistors,Potentiometer,A/DandD/Aconvertors,LEDs	50	50
6	EC8562 DIGITAL SIGNAL PROCESSING LABORATORY	PCs with Fixed / Floating point DSP Processors (Kit/Add-onCards)	15	15
		MATLAB with Simulink and Signal ProcessingToo lBoxor Equivalent Software in desktop systems	15	15
		Signal Generators(1MHz)	20	20
		CRO(20MHz)	20	20
7	EC8561 COMMUNICATION SYSTEMS LABORATORY	Kits for Signal Sampling,TDM, AM, FM,PCM, DM and Line Coding Schemes,Errorcontrol code	14	14
		CROs	15	15
		MATLAB/SCILA Bore quivalent software package for simulation experiments	20	20
		PCs	20	20
		Probes(CRO)	30	30
		Patchcords	100	100
		MSO	4	4
		DSO	4	4
8	EC8563 COMMUNICATION NETWORKS LABORATORY	C/ Python/Java/EquivalentCompiler	30	30
		Standard LAN Trainer Kits	4	4
		Qualnet/Optisim/Matlab/NS2/Netsim	30	30
		PCs	30	30
9	EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY	8086 Microprocessor trainer kit with power supply	15	15
		8051Microcontroller trainer kit	15	15
		Traffic light control interfacing card compatible with 8086&8051kits	5	5
		Stepper motor control interfacing compatible with 8086&8051kits	5	5
		Digital clock interfacing board compatible with 8086&8051kits	5	5
		Keyboard&Display interface board compatible with 8086&8051kits	5	5
		Printer interfacing card compatible with 8086 & 8051kits	5	5
		A/DandD/Ainterfacing card compatible with 8086 & 8051kits	5	5
		Serial and Parallel interfacing card compatiblewith 8086&8051kits	5	5
10	EC8661 VLSI DESIGN LABORATORY	XilinxISE/AlteraQuartus/equivalentEDATools	10	10
		Xilinx/Altera/equivalentFPGABoards	10	10
		Cadence/Synopsis/MentorGraphics/Tanner/equivalentE DATools	10	10

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		PersonalComputer	30	30
12	EC8711 EMBEDDED LABORATORY	Embedded trainer kits with ARM board	10	10
		Embedded trainer kits suitable for wireless communication	10	10
		Adequate quantities of Hardware,software and consumables	10	10
	EC8761 ADVANCED COMMUNICATION LABORATORY	Trainer kit for carrying out LED and PIN diode characteristics,Digital multimeter,optical power meter	2	2
		Trainer kit for determining the mode characteristics,losses in optical fiber	2	2
		Trainer kit for analyzing Analog andDigitallinkperformance,2MbpsPRBSData source,10 MHzsignalgenerator, 20MHzDigitalstorageOscilloscope	2	2
		Kit for measuring Numerical apertureandAttenuation of fiber	2	2
		Advanced Optical fiber trainer kit for PC to PC communication, BER Measurement,Pulse broadening	2	2
		MM/SM Glass and plastic fiber patch chord swith ST/SC/E2000connectors	2	2
		LED swith ST/SC/ E2000 receptacles –650/850nm	2	2
		PINPDswithST/SC/E2000 receptacles–650/850nm	2	2
		Digital Communications Teaching Bundle (LabVIEW/MATLAB/Equivalent softwaretools)	10	10
		Software Define Radio Transceiver Platform with antennas and accessories	2	2

Electronics and Instrumentation Engineering:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	EE8261 ELECTRIC CIRCUITS LABORATORY	Regulated Power Supply: 0 - 15 V D.C	10	10
		Function Generator (1 MHz)	10	10
		Single Phase Energy Meter	1	1
		Oscilloscope (20 MHz).	10	10
		Digital Storage Oscilloscope (20 MHz)	1	1
		PC with Circuit Simulation Software	10	10
		e-Sim / Scilab/ Pspice / Matlab /other Equivalent software Package)	10	10
		Printer	1	1
		AC/DC - Voltmeters	10	10
		Ammeters	10	10

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Multi-meters	10	10
		Single Phase Wattmeter	3	3
		Decade Resistance Box, Decade Inductance Box, Decade Capacitance Box (Each).	6	6
		Circuit Connection Boards	10	10
2	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Net beans or Eclipse	30	30
3	EI8361 MEASUREMENTS AND TRANSDUCERS LABORATORY	Measurement of Linear displacement usingPotentiometer	1	1
		Strain gauge and Load cell Characterisationandapplication	1	1
		LVDTCharacterisationandapplication	1	1
		HalleffectCharacterisationandapplication	1	1
		MeasurementofAngulardisplacement	1	1
		Mufflefurnace	1	1
		ThermistorCharacterisationandapplication	1	1
		Various types Thermocouple and RTDCharacterisationand application	1	1
		Measurementofpowerandenergy	1	1
		Sufficient number power supply,Galvanometer,Breadboard,Multimeter,Resistors,Decade	15	15
		Sufficient number Capacitance box, Decade resistance box,Decade Inductance box,CRO	15	15
4	EE8461 LINEAR AND DIGITAL INTEGRATED CIRCUITS LABORATORY	Dual,(0-30V)variablePowerSupply	10	10
		CRO(30MHz)	9	9
		DigitalMultimeter	10	10
		FunctionGenerator(1MHz)	8	8
		ICTester(Analog)	2	2
		Breadboard	10	10
		Computer(PSPICEinstalled)	1	1
		IC741/ICNE555/566/565	10	10
		DigitalICtypes	10	10
		LED	10	10
		LM317	10	10
		LM723	10	10

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		ICSG3524/SG3525	10	10
		Transistor2N3391	10	10
		Diodes(IN4001,BY126)	10	10
		Zenerdiodes	10	10
		Potentiometer	10	10
		Step-downtransformer(230v/12-0-12v)	10	10
		Capacitor	10	10
		Resistors1/4WattAssorted	10	10
		SingleStrandWire	10	10
5	EI8461 DEVICES AND MACHINES LABORATORY	CircuitSimulationSoftware(5Users)	5	5
		(Pspice/Matlab/otherEquivalentsoftware Package) with PC	30	30
		Sufficient number of powe rsupply, Galvanometer, Breadboard,Multimeter,	10	10
		Semiconductor devices like Diode, ZenerDiode,NPN Transistors,JFET, andUJT	10	10
		DC ShuntMotorwithLoading Arrangement	3	3
		SinglePhaseTransformer	3	3
		Single PhaseInductionMotorwithLoading Arrangement	1	1
		Single PhaseAuto Transformer	3	3
		Single PhaseResistiveLoadingBank	2	2
		Ammeters	2	2
		Voltmetersormultimeters	2	2
		Switches	2	2
		Tachometers	2	2
		Wattmeters	2	2
6	EE8681 MICROPROCESSORS AND MICROCONTROLLERS LABORATORY	8085MicroprocessorTrainerwithPowerSupply	15	15
		8051Micro ControllerTrainerKitwith powersupply	15	15
		8255Interfaceboard	5	5
		8251Interfaceboard	5	5
		8259Interfaceboard	5	5
		8279Keyboard/DisplayInterfaceboard	5	5
		8254timercounter	5	5

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		ADC andDACcard	5	5
		AC &DC motorwithController	5	5
		TrafficLightControlSystem	5	5
7	EI8561 INDUSTRIAL INSTRUMENTATION LABORATORY	Orificeplate	1	1
		Deadweighttesterwith pressure gauge	1	1
		Torquetrainer	1	1
		SayboltViscometer	1	1
		Vacuum gauge	1	1
		DPtransmitter	1	1
		UVVisiblespectrophotometer	1	1
		pHmeter	1	1
		Conductivitymeter	1	1
		ECGtrainer	1	1
		Pulse ratetrainer	1	1
		tachometer	1	1
8	CS8381 DATA STRUCTURES LABORATORY	SystemswithLinuxOperatingSystemwithgnucompiler	30	30
9	EI8661 PROCESS CONTROL LABORATORY	Flowprocess stationwith allaccessories	1	1
		Analog/DigitalPIDcontroller	2	2
		Control valve trainer (withpositionfor varying Pacrossthevalve)	1	1
		Flowmeter	1	1
		Levelprocessstationwithallaccessories	1	1
		Temperatureprocessstationwithall accessories	1	1
		Pressureprocessstationwithallaccessories	1	1
		MATLABsoftware	Minimum 10 user license	10 User license
		Personalcomputer	15	15
10	EI8761 INDUSTRIAL AUTOMATION LABORATORY	ProgrammableLogiccontroller	5	5
		ProgrammableLogiccontrollerSoftware	10	10
		DAQ card	2	2

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Filling/DrainingSystem	1	1
		TrafficLightController	2	2
		DC Motor	5	5
		Personalcomputer	10	10
		DCSalongwithInterfacemodules	1	1
		ThermalProcess	1	1
		LevelProcess	1	1
		FlowProcessstations	1	1
		SmartTransmitter	1	1
11	EI8762 INSTRUMENTATION SYSTEM DESIGN LABORATORY	Sufficient number of MonolithicInstrumentation amplifier, Operational amplifiers, IC7805 and resistors, diodes,capacitors	15	15
		Linear contro lvalve,ON/OFFcontrolvalve,Airregulator, Rotameter,Pump	1 each	1
		SufficientnumberofIC741, CRO, Breadboard, Signal generator (PID)Microprocessor kit with ADC and DACsection	15	15
		Any Process station (Temperature or Level) with Corresponding sensors, Data acquisition card,and Storage device(micro controller/microprocessor)	1	1
		Flow process station with DP transmitter	1	1
		Loopanalyzer	1	1
		Thermocouple& RTD	Minimum 1	1
		Bonded strain gauge,Loads	Minimum 1	1
		orificeplate	Minimum 1	1

Information Technology:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	IT8211 Information Technology Essentials Laboratory	PCwithLinux/Windows/Solaris/MacOSXoperatingsystem	30	30
		XAMPPWebserver	1	1
		Mobile App Development tool(LikeappInventor)	1	1
2	CS8261 C Programming Laboratory	Systems with Linux Operating System with gnu compiler	30	30
3	CS8382 DIGITAL SYSTEMS LABORATORY	Digitaltrainerkits	30	30
		DigitalIcs	30	30
		Software:HDL simulator	30	30
4	CS8381 DATA STRUCTURES	Systems with Linux Operating System with gnu compiler	30	30

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
	LABORATORY			
5	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Netbeans or Eclipse	30	30
6	CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY	SystemswithMySql	30	30
		VisualStudio	30	30
		Server	1	1
7	CS8461 OPERATING SYSTEMS LABORATORY	Systems with Linux OS and GNU Computer	30	30
8	EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY	8086Microprocessortrainerkitwithpower supply	15	15
		8051Microcontrollertrainerkit	15	15
		Traffic light control interfacing card compatible with 8086&8051kits	5	5
		Stepper motor control interfacing compatible with 8086&8051kits	5	5
		Digital clock interfacing board compatible with 8086&8051kits	5	5
		Keyboard&Display interface board compatible with 8086&8051kits	5	5
		Printer interfacing card compatible with 8086 & 8051kits	5	5
		A/DandD/A interfacing card compatible with 8086 & 8051kits	5	5
		Serial and Parallel interfacing cardcompatible with 8086&8051kits	5	5
9	CS8581 NETWORKS LABORATORY	StandaloneDesktops	30	30
		C / C++ / Java / Python / EquivalentCompilerNetwork SimulatorlikeNS2/Glomosim / OPNEt / Packet Tracer /Equivalent	30	30
10	IT8511 WEB TECHNOLOGY LABORATORY	Dream Weaver or Equivalent, MySQL or Equivalent, Apache Server,WAMP/XAMPP	30	**30**
		Standalonedesktops	30	**30**
11	CS8582 OBJECT ORIENTED ANALYSIS AND DESIGN LABORATORY	Rational Suite(UserLicense)	30	30
		ArgoUML,StarUML, Visual Paradigm(Or)Equivalent EclipseIDEand Junit	30	30
		PCs	30	30
12	CS8662 MOBILE APPLICATION DEVELOPMENT LABORATORY	Stand alone desktops with Windows or Android or iOS or Equivalent Mobile Application Development Tools with appropriate emulators and debuggers	30	30
13	IT8761 SECURITY LABORATORY	C / C++ / Java or equivalent compiler GnuPG, Snort, N-Stalkeror Equivalent	30	30
		Standalonedesktops	30	30

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
14	IT8711 FOSS AND CLOUD COMPUTING LABORATORY	PCwithlatest version	30	30
		Cloud tools from free of open sourcelike open nebula, open stack, Eucalyptus software	30	30

Computer Science and Business Systems:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	AD8261 DATA STRUCTURES DESIGN LABORATORY	Python 3 interpreter for Windows/Linux	30	30
2	CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY	Java Interpreter for Windows or Linux	30	30
3	CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY	System with MYSQL	30	30
4	CW8311 BUSINESS COMMUNICATION AND VALUE SCIENCE LABORATORY I	System with Windows 7	30	30
		Speaker	1	1
		Headphones	30	30
		Projector	1	1
5	CS 8461 OPERATING SYSTEMS LABORATORY	System with Linux OS	30	30
6	CW 8411 COMPUTATIONAL STATISTICS LABORATORY	R and R Studio for Windows or Linux	30	30
7	CW8412 BUSINESS COMMUNICATION AND VALUE SCIENCE LABORATORY II	System with Windows 7	30	30
		Speaker	1	1
		Headphones	30	30
		Projector	1	1
8	CW8512 SOFT SKILLS LABORATORY	System with Hi Class Software	30	30
9	CW8511 MINI PROJECT (SOFTWARE / SYSTEM DESIGN/ARCHITECTURE) END TO END	System with Windows	30	30
10	CW 8611 BUSINESS ANALYTICS LABORATORY DESIGN/ARCHITECTURE) END TO END	R and R Studio for Windows or Linux	30	30

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
11	CW 8612 ARTIFICIAL INTELLIGENCE LABORATORY	System with Turbo C and LISP and PROLOG	30	30

Artificial Intelligence and Machine Learning:

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
1	21GE111 C PROGRAMMING LABORATORY	Systems with Linux Operating System and gnu compiler	30	30
2	21CS211 DATA STRUCTURES LABORATORY	Systems with Linux Operating System and gnu compiler	30	30
3	21CS202 PYTHON PROGRAMMING (LAB INTEGRATED)	Systems with Python Compiler	30	30
4	21IT412 DATABASE MANAGEMENT SYSTEMS LABORATORY	Systems with MySql	30	30
		Visual Studio / Eclipse	30	30
		Server	1	1
5	21CS311 OBJECT ORIENTED PROGRAMMING LABORATORY	Systems with either Netbeans or Eclipse	30	30
6	21AM302 PRINCIPLES OF ARTIFICIAL INTELLIGENCE	Machines with intel i5/i7 processor and minimum 8 GB RAM	30	30
		R/Python	30	30
		Keras/scikit-learn/Pytorch/Tensor Flow	30	30
7	21AM404 OPERATING SYSTEM FUNDAMENTALS (LAB INTEGRATED)	Systems with Linux OS and gnu compiler	30	30
8	21AM411 MACHINE LEARNING LABORATORY	Machines with intel i5/i7 processor and minimum 8 GB RAM	30	30
		R/Python	30	30
		Keras/scikit-learn/Pytorch/Tensor Flow	30	30
9	21AM412 DATA ANALYTICS LABORATORY	Machines with intel i5/i7 processor and minimum 8 GB RAM	30	30
		Hadoop Framework, Map Reduce Framework/ Spark/ Hive/ Pig	30	30

Sl. No	Name of the Laboratory	Description of Equipment	Quantity Required	Quantity Available
		Anaconda / R Studio or Equivalent	30	30

List of Experimental Setup in each Laboratory/Workshop

Computer Science and Engineering

22CS101 PROBLEM SOLVING USING C++ (Lab Integrated)

1. Write C/C++ programs for the following:
   a. Find the sum of individual digits of a positive integer.
   b. Compute the GCD of two numbers.
   c. Find the roots of a number (Newton_s method)
2. Write C/C++ programs using arrays:
   a. Find the maximum of an array of numbers.
   b. Remove duplicates from an array of numbers.
   c. Print the numbers in an array after removing even numbers.
3. Write C/C++ programs using strings:
   a. Checking for palindrome.
   b. Count the occurrences of each character in a given word
4. Generate salary slip of employees using structures and pointers. Create a structure Employee with the following members: EID, Ename, Designation, DOB, DOJ, Basic pay

   Note that DOB and DOJ should be implemented using structure within structure.
5. Compute internal marks of students for five different subjects using structures and functions.
6. Write a program Illustrating Class Declarations, Definition, and Accessing Class Members.
7. Program to illustrate default constructor, parameterized constructor and copy constructors.
8. Write a Program to Demonstrate the i) Operator Overloading. ii) Function Overloading.
9. Write a Program to Demonstrate Friend Function and Friend Class.
10. Program to demonstrate inline functions.
11. Program for Overriding of member functions.
12. Write C++ programs that illustrate how the following forms of inheritance are supported:
    a) Single inheritance b) Multiple inheritance c) Multi level inheritance d)Hierarchical inheritance.
13. Program to demonstrate pure virtual function implementation.
14. Count the number of account holders whose balance is less than the minimum balance using sequential. access file.
15. Write a Program to Demonstrate the Catching of all Exceptions.

16. Mini project

22CS102 SOFTWARE DEVELOPMENT PRACTICES (Lab Integrated)

List of Exercise/Experiments:

1. Form a Team, Decide on a project:
    a) Create a repository in GitHub for the team.
    b) Choose and follow a Git workflow.
        - Each team member can create a StudentName.txt file with contents about themselves and the team project.
        - Each team member can create a branch, commit the file with a proper commit message and push the branch to remote GitHub repository.
        - Team members can now create a Pull request to merge the branchto master branch or main development branch.
        - The Pull request can have two reviewers, one peer team member and one faculty. Reviewers can give at least one comment for Pull Request updating.
        - Once pull request is reviewed and merged, the master or main development branch will have files created by all team members.
2. Create a web page with at least three links to different web pages. Each of the web pages is to be designed by a team member. Follow Git workflow, pull requestand peer reviews.
3. Form a Team, Decide on a project:
    c) Create a repository in GitHub for the team.
    d) Choose and follow a Git workflow.
        - Each team member can create a StudentName.txt file with contents about themselves and the team project
        - Each team member can create a branch, commit the file with a proper commit message and push the branch to remote GitHub repository.
        - Team members can now create a Pull request to merge the branchto master branch or main development branch.
        - The Pull request can have two reviewers, one peer team member and one faculty. Reviewers can give at least one comment for Pull Request updation.
        - Once pull request is reviewed and merged, the master or main development branch will have files created by all team members.
4. Create a web page with at least three links to different web pages. Each of the web pages is to be designed by a team member. Follow Git workflow, pull requestand peer reviews.

5. Create web pages using the following:

    - Tables and Lists
    - Image map
    - Forms and Form elements
    - Frames

6. Apply Cascading style sheets for the web pages created.

7. Form Validation (Date, Email, User name, Password and Number validation)using JavaScript.

8. Implement Event Handling in the web pages.

Mini Projects-

Develop any one of the following web applications (not limited to one) using above technologies.

- Online assessment system
- Ticket reservation system
- Online shopping
- Student management system
- Student result management system
- Library management
- Hospital management
- Attendance management system
- Examination automation system
- Web based chat application

22EC101 DIGITAL PRINCIPLESAND SYSTEMS DESIGN (Lab Integrated)

1. Implementation of Boolean expression using logic gates
2. Design of adders
3. Design of subtractors.
4. Design of binary adder using IC7483
5. Design of Multiplexers &Demultiplexers.
6. Design of Encoders and Decoders.
7. Implementation of a Boolean function using a multiplexer
8. Design and implementation of 3-bit ripple counters.
9. Design and implementation of 3-bit synchronous counter
10. Design and implementation of shift registers

22CS201 DATA STRUCTURES (Lab Integrated)

1. Array implementation of List ADTs.
2. Linked list implementation of List ADTs.
3. Array implementation of Stack and Queue ADTs.
4. Linked list implementation of Stack and Queue ADTs.
5. Applications of List – Polynomial manipulations
6. Applications of Stack – Infix to postfix conversion and expression evaluation.
7. Implementation of Binary Trees and operations of Binary Trees.
8. Implementation of Binary Search Trees.
9. Implementation of Heaps using Priority Queues
10. Graph representation and Traversal algorithms.
11. Implement searching and sorting algorithms.

22CS202 JAVA PROGRAMMING (Lab Integrated)

1. Develop a Java application to generate Electricity bill. You must use one super class called EB Bill and must have two sub classes namely Domestic Bill and Commercial Bill. Create a class with the following members: Consumer no., consumer name, previous month reading, current month reading, type of EB connection (i.e domestic or commercial). Compute the bill amount using the following tariff

If the type of the EB connection is domestic, calculate the amount to be paid as follows: First 100 units - Rs. 1 per unit

101-200 units - Rs. 2.50 per unit 201 -500 units - Rs. 4 per unit

> 501 units - Rs. 6 per unit

If the type of the EB connection is commercial, calculate the amount to be paid as follows: First 100 units - Rs. 2 per unit

101-200 units - Rs. 4.50 per unit 201 -500 units - Rs. 6 per unit

> 501 units - Rs. 7 per unit

2.Arrays Manipulations: (Use Methods for implementing these in a Class)

Find kth smallest element in an unsorted array

Find the sub array with given sum

Matrix manipulations – Addition, Subtraction, Multiplication

Remove duplicate elements in an Array

3.Accept an integer value N and print the Nth digit in the integer sequence 1,2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15 and so on till infinity.

Example: The 11th digit in the sequence 12345678910111213…. is 0.

4. Develop a Java application to implement currency converter (Dollar to INR, EURO to INR, Yen to INR and vice versa), distance converter (meter to KM, milesto KM and vice versa), time converter (hours to minutes, seconds and vice versa)using packages.

5. Develop a Java application with Employee class with Emp_name, Emp_id, Address, Mail_id, Mobile_no as members. Inherit the classes, Programmer, Assistant Professor, Associate Professor and Professor from employee class. AddBasic Pay (BP) as the member of all the inherited classes with 97% of BP as DA, 10 % of BP as HRA, 12% of BP as PF, 0.1% of BP for staff club fund. Generate pay slips for the employees with their gross and net salary.

6.. Design a Java interface for ADT Stack. Implement this interface using array and built-in classes. Provide necessary exception handling in both the implementations.

7. Write a Java Program to create an abstract class named Shape that contains two integers and an empty method named print Area(). Provide three classes named Rectangle, Triangle and Circle such that each one of the classes extends the class Shape. Each one of the classes contains the methods print Area () that prints the area of the given shape and Numberofsides() that prints the number of sides of the given shape.

8. Write a Java program to apply built-in and user defined exceptions.

9. Write a Java program to read and copy the content of one file to other by handling all file related exceptions.

10.String Manipulation:

a. Reversing a set of words and count the frequency of each letter in the string.

b. Pattern Recognition - Find the number of patterns of form 1[0]1 where [0] represents any number of zeroes (minimum requirement is one 0) there shouldnot be any other character except 0 in the [0] sequence in a given binary string.

c. Remove all the occurrences of string S2 in string S1 and print the remaining.

d. Find the longest repeating sequence in a string

e. Print the number of unique string values that can be formed by rearranging the letters in the string S.

11. Write a Java program that correctly implements producer consumer problem using the concept of inter thread communication.

12. Collections:

a. Write a program to perform string operations using ArrayList. Write functions forthe following

i. Append - add at end

ii. Insert – add at particular index

iii. Search

iv. List all string starts with given letter

b. Find the frequency of words in each text.

22IT202 DATABASE MANAGEMENT SYSTEM (Lab Integrated)

1. Data Definition Commands, Data Manipulation Commands for inserting deleting, updating and retrieving Tables and Transaction Control statements Write programs using the following system calls of UNIX operating systemfork, exec, getpid, exit, wait, close, stat, opendir, readdir
2. Database Querying – Simple queries, Nested queries, Sub queries and Joins
3. Views, Sequences, Synonyms
4. Database Programming: Implicit and Explicit Cursors.
5. Procedures and Functions
6. Triggers
7. Exception Handling
8. Database Design using ER modeling, normalization and Implementation for any application
9. Database Connectivity with Front End Tools
10. Case Study using real life database applications anyone from the following list
    - Inventory Management for a EMart Grocery Shop
    - Society Financial Management
    - Cop Friendly App – Eseva
    - Property Management – eMall
    - Star Small and Medium Banking and Finance
11. Build Entity Model diagram. The diagram should align with the business and functional goals stated in the application.
12. Apply Normalization rules in designing the tables in scope.
13. Prepared applicable views, triggers (for auditing purposes), functions for enabling enterprise grade features.

14. Build PL SQL / Stored Procedures for Complex Functionalities, ex EOD Batch Processing for calculating the EMI for Gold Loan for each eligible Customer.
15. Ability to showcase ACID Properties with sample queries with appropriate settings

22CS301 ADVANCE JAVA PROGRAMMING (Lab Integrated)

1. Write Java programs to implement Queue interface
    - Demonstrate the working of Dequeue.
    - Demonstrate the working of ArrayQueue.
2. Write Java programs using Utilities
    - String Processing.
    - Date Manipulation.
3. Write Java programs using Comparable & Observer
4. Write a Java program to display a sting in a frame
5. Write a Java program to change the color, font name, style and size of a string
6. Write a Java program to demonstrate mouse events
7. Write a Java program to demonstrate adapter classes
8. Write a Java program to design a calculator
9. Write a Java program to demonstrate event handlingCount the occurrences of each character in a given word.
10. Write programs in Java using Servlets:
    - To invoke servlets from HTML forms
    - Session tracking.
11. Create a dynamic web application using Servlet/JSP with a facility to
    - Login to the application
    - Register a new user and
    - Change password for an existing user
12. i. Validate the form using PHP regular expression.
13. ii. PHP stores a form data into database.
14. Write a web service for finding public review about a consumer product.
15. Write a web service for finding what people think by asking 500 people consumer product.
16. Mini Projects-Develop any one of the following web applications using above technologies.
    - Online assessment system
    - Ticket reservation system
    - Online shopping

EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY

8086 Programs using kits and MASM

1. Basic arithmetic and Logical operations
2. Move a data block without overlap
3. Code conversion, decimal arithmetic and Matrix operations.
4. Floating point operations, string manipulations, sorting and searching
5. Password checking, Print RAM size and system date
6. Counters and Time Delay

Peripherals and Interfacing Experiments

7. Traffic light controller
8. Stepper motor control
9. Digital clock
10. Key board and Display
11. Printer status
12. Serial interface and Parallel interface
13. A/D and D/A interface and Waveform Generation

8051 Experiments using kits and MASM

14. Basic arithmetic and Logical operations
15. Square and Cube program, Find 2‘s complement of a number
16. Unpacked BCD to ASCII

CS8582 OBJECT ORIENTED ANALYSIS AND DESIGN LABORATORY

Draw standard UML diagrams using an UML modeling tool for a given case study and map designto code and implement a 3 layered architecture. Test the developed code and validate whether theSRS is satisfied.

1. Identify a software system that needs to be developed.
2. Document the Software Requirements Specification (SRS) for the identified system.
3. Identify use cases and develop the Use Case model.
4. Identify the conceptual classes and develop a Domain Model and also derive a Class Diagram from that.
5. Using the identified scenarios, find the interaction between objects and represent themusing UML Sequence and Collaboration Diagrams
6. Draw relevant State Chart and Activity Diagrams for the same system.
7. Implement the system as per the detailed design
8. Test the software system for all the scenarios identified as per the usecase diagram
9. Improve the reusability and maintainability of the software system by applying appropriatedesign patterns.
10. Implement the modified system and test it for various scenarios

SUGGESTED DOMAINS FOR MINI-PROJECT:

1. Passport automation system.
2. Book bank
3. Exam registration
4. Stock maintenance system.
5. Online course reservation system
6. Airline/Railway reservation system
7. Software personnel management system
8. Credit card processing
9. e-book management system
10. Recruitment system
11. Foreign trading system
12. Conference management system
13. BPO management system
14. Library management system
15. Student information system

CS8661 INTERNET PROGRAMMING LABORATORY

1. Create a web page with the following using HTML
   a. To embed a map in a web page
   b. To fix the hot spots in that map
   c. Show all the related information when the hot spots are clicked.
2. Create a web page with the following.
   a. Cascading style sheets.
   b. Embedded style sheets.
   c. Inline style sheets. Use our college information for the web pages.
3. Validate the Registration, user login, user profile and payment by credit card pages usingJavaScript.
4. Write programs in Java using Servlets:
   i. To invoke servlets from HTML forms
   ii. Session tracking using hidden form fields and Session tracking for a hit count
5. Write programs in Java to create three-tier applications using servlets for conducting on-line examination for displaying student mark list. Assume that student information is available in a database which has been stored in a database server.
6. Install TOMCAT web server. Convert the static web pages of programs into dynamic web pages using servlets (or JSP) and cookies. Hint: Users information (user id, password, credit card number) would be stored in web.xml. Each user should have a separate

   Shopping Cart.

7. Redo the previous task using JSP by converting the static web pages into dynamic web pages. Create a database with user information and books information. The books catalogue should be dynamically loaded from the database.

8. Create and save an XML document at the server, which contains 10 users Information. Write a Program, which takes user Id as an input and returns the User details by takingthe user information from the XML document

9. i. Validate the form using PHP regular expression.
   ii. PHP stores a form data into database.

10. Write a web service for finding what people think by asking 500 people‘s opinion for anyconsumer product.

CS8662 MOBILE APPLICATION DEVELOPMENT LABORATORY

1. Develop an application that uses GUI components, Font and Colours
2. Develop an application that uses Layout Managers and event listeners.
3. Write an application that draws basic graphical primitives on the screen.
4. Develop an application that makes use of databases.
5. Develop an application that makes use of Notification Manager
6. Implement an application that uses Multi-threading
7. Develop a native application that uses GPS location information
8. Implement an application that writes data to the SD card.
9. Implement an application that creates an alert upon receiving a message
10. Write a mobile application that makes use of RSS feed
11. Develop a mobile application to send an email.
12. Develop a Mobile application for simple needs (Mini Project)

IT8761 SECURITY LABORATORY

1. Perform encryption, decryption using the following substitution techniques
   (i) Ceaser cipher, (ii) playfair cipher iii) Hill Cipher iv) Vigenere cipher
2. Perform encryption and decryption using following transposition techniques
   i) Rail fence ii) row & Column Transformation
3. Apply DES algorithm for practical applications.
4. Apply AES algorithm for practical applications.

5. Implement RSA Algorithm using HTML and JavaScript
6. Implement the Diffie-Hellman Key Exchange algorithm for a given problem.
7. Calculate the message digest of a text using the SHA-1 algorithm.
8. Implement the SIGNATURE SCHEME - Digital Signature Standard.
9. Demonstrate intrusion detection system (ids) using any tool eg. Snort or any other s/w.
10. Automated Attack and Penetration Tools

    Exploring N-Stalker, a Vulnerability Assessment Tool

11. Defeating Malware

    i) Building Trojans ii) Rootkit Hunter

CS8711 CLOUD COMPUTING LABORATORY

1. Install Virtualbox/VMware Workstation with different flavours of linux or windows OS on top of windows 7 or 8.
2. Install a C compiler in the virtual machine created using virtual box and execute SimplePrograms
3. Install Google App Engine. Create hello world app and other simple web applications using python/java.
4. Use GAE launcher to launch the web applications.
5. Simulate a cloud scenario using CloudSim and run a scheduling algorithm that is not present in CloudSim.
6. Find a procedure to transfer the files from one virtual machine to another virtual machine.
7. Finda procedure to launch virtual machine using trystack (Online Openstack DemoVersion)
8. Install Hadoop single node cluster and run simple applications like word count.

Electronics and Communication Engineering

EC8261 CIRCUITS AND DEVICES LABORATORY

1. Characteristics of PN Junction Diode
2. Zener diode Characteristics & Regulator using Zener diode
3. Common Emitter input-output Characteristics
4. Common Base input-output Characteristics
5. FET Characteristics
6. SCR Characteristics
7. Clipper and Clamper & FWR
8. Verifications Of Thevinin & Norton theorem

9. Verifications Of KVL & KCL
10. Verifications Of Super Position Theorem
11. verifications of maximum power transfer & reciprocity theorem
12. Determination Of Resonance Frequency of Series & Parallel RLC Circuits
13. Transient analysis of RL and RC circuits

EC8361 ANALOG AND DIGITAL CIRCUITS LABORATORY

1. Design of Regulated Power supplies
2. Frequency Response of CE, CB, CC and CS amplifiers
3. Darlington Amplifier
4. Differential Amplifiers - Transfer characteristics, CMRR Measurement
5. Cascode and Cascade amplifiers
6. Determination of bandwidth of single stage and multistage amplifiers
7. Analysis of BJT with Fixed bias and Voltage divider bias using Spice
8. Analysis of FET, MOSFET with fixed bias, self-bias and voltage divider bias using simulation software like Spice
9. Analysis of Cascode and Cascade amplifiers using Spice
10. Analysis of Frequency Response of BJT and FET using Spice

EC8381 FUNDAMENTALS OF DATA STRUCTURES IN C LABORATORY

1. Basic C Programs – looping, data manipulations, arrays
2. Programs using strings – string function implementation
3. Programs using structures and pointers
4. Programs involving dynamic memory allocations
5. Array implementation of stacks and queues
6. Linked list implementation of stacks and queues
7. Application of Stacks and Queues
8. Implementation of Trees, Tree Traversals
9. Implementation of Binary Search trees
10. Implementation of Linear search and binary search
11. Implementation Insertion sort, Bubble sort, Quick sort and Merge Sort
12. Implementation Hash functions, collision resolution technique

EC8461 CIRCUITS DESIGN AND SIMULATION LABORATORY

DESIGN AND ANALYSIS OF THE FOLLOWING CIRCUITS

Series and Shunt feedback amplifiers-Frequency response, Input and outputimpedance

RC Phase shift oscillator and Wien Bridge Oscillator

Hartley Oscillator and Colpitts Oscillator

Single Tuned Amplifier

RC Integrator and Differentiator circuits

Astable and Monostable multivibrators

Clippers and Clampers

SIMULATION USING SPICE (Using Transistor):

Tuned Collector Oscillator

Twin -T Oscillator / Wein Bridge Oscillator

Double and Stagger tuned Amplifiers

Bistable Multivibrator

Schmitt Trigger circuit with Predictable hysteresis

Analysis of power amplifier

EC8462 LINEAR INTEGRATED CIRCUITS LABORATORY

DESIGN AND TESTING OF THE FOLLOWING CIRCUITS

Inverting, Noninverting and differential amplifiers.

IntegratorandDifferentiator.

Instrumentationamplifier

Activelow-pass,High-pass andband-passfilters.

Astable& Monostablemultivibrators using Op-amp

SchmittTriggerusingop-amp.

PhaseshiftandWienbridgeoscillatorsusingOp-amp.

AstableandMonostablemultivibratorsusing NE555Timer.

PLLcharacteristics andits use as Frequency Multiplier, Clock synchronization

R-2R LadderType D-A Converter using Op-amp.

DC power supply using LM317 and LM723.

Study of SMPS

SIMULATION USING SPICE:

Active low-pass,High-pass and band-pass filters using Op-amp

Astable and Mono stable multivibrator susing NE555 Timer.

A/D converter

Analog multiplier

EC8562 DIGITAL SIGNAL PROCESSING LABORATORY

LIST OF EXPERIMENTS: MATLAB / EQUIVALENT SOFTWARE PACKAGE

1. Generation of elementary Discrete-Time sequences
2. Linear and Circular convolutions
3. Auto correlation and Cross Correlation
4. Frequency Analysis using DFT
5. Design of FIR filters (LPF/HPF/BPF/BSF) and demonstrates the filtering operation
6. Design of Butterworth and Chebyshev IIR filters (LPF/HPF/BPF/BSF) and demonstrate the filtering operations

DSP PROCESSOR BASED IMPLEMENTATION

1. Study of architecture of Digital Signal Processor
2. Perform MAC operation using various addressing modes
3. Generation of various signals and random noise
4. Design and demonstration of FIR Filter for Low pass, High pass, Band pass and Band stop filtering
5. Design and demonstration of Butter worth and Chebyshev IIR Filters for Low pass, High pass, Band pass and Band stop filtering
6. Implement an Up-sampling and Down-sampling operation in DSP Processor

EC8561 COMMUNICATION SYSTEMS LABORATORY

1. Signal Sampling and reconstruction
2. Time Division Multiplexing
3. AM Modulator and Demodulator
4. FM Modulator and Demodulator
5. Pulse Code Modulation and Demodulation
6. Delta Modulation and Demodulation
7. Line coding schemes
8. Simulation of ASK, FSK, and BPSK generation schemes
9. Simulation of DPSK, QPSK and QAM generation schemes
10. Simulation of signal constellations of BPSK, QPSK and QAM
11. Simulation of ASK, FSK and BPSK detection schemes
12. Simulation of Linear Block and Cyclic error control coding schemes
13. Simulation of Convolutional coding scheme
14. Communication link simulation

EC8563 COMMUNICATION NETWORKS LABORATORY

1. Implementation of Error Detection / Error Correction Techniques
2. Implementation of Stop and Wait Protocol and sliding window
3. Implementation and study of Goback-N and selective repeat protocols
4. Implementation of High Level Data Link Control
5. Implementation of IP Commands such as ping, Traceroute, nslookup.
6. Implementation of IP address configuration.
7. To create scenario and study the performance of network with CSMA / CA protocoland compare with CSMA/CD protocols.
8. Network Topology - Star, Bus, Ring
9. Implementation of distance vector routing algorithm
10. Implementation of Link state routing algorithm
11. Study of Network simulator (NS) and simulation of Congestion Control Algorithms using NS
12. Implementation of Encryption and Decryption Algorithms using any programming language

EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY

8086 Programs using kits and MASM

1. Basic arithmetic and Logical operations

2. Move a data block without overlap
3. Code conversion, decimal arithmetic and Matrix operations.
4. Floating point operations, string manipulations, sorting and searching
5. Password checking, Print RAM size and system date
6. Counters and Time Delay

Peripherals and Interfacing Experiments

7. Traffic light controller
8. Stepper motor control
9. Digital clock
10. Keyboard and Display
11. Printer status
12. Serial interface and Parallel interface
13. A/D and D/A interface and Waveform Generation

8051 Experiments using kits and MASM

14. Basic arithmetic and Logical operations
15. Square and Cube program, Find 2's complement of a number
16. Unpacked BCD to ASCII

EC8661 VLSI DESIGN LABORATORY

Part I: Digital System Design using HDL & FPGA (24 Periods)

1. Design an Adder (Min 8 Bit) using HDL. Simulate it using Xilinx/Altera Software andimplement by Xilinx/Altera FPGA
2. Design a Multiplier (4 Bit Min) using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA
3. Design an ALU using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA
4. Design a Universal Shift Register using HDL. Simulate it using Xilinx/Altera Softwareand implement by Xilinx/Altera FPGA
5. Design Finite State Machine (Moore/Mealy) using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA
6. Design Memories using HDL. Simulate it using Xilinx/Altera Software and implementby Xilinx/Altera FPGA
    a. Compare pre synthesis and post synthesis simulation for experiments 1 to 6.

Requirements: Xilinx ISE/Altera Quartus/ equivalent EDA Tools along with Xilinx/Altera/equivalent FPGA Boards

Part-II Digital Circuit Design (24 Periods)

7. Design and simulate a CMOS inverter using digital flow
8. Design and simulate a CMOS Basic Gates & Flip-Flops

9. Design and simulate a 4-bit synchronous counter using a Flip-Flops

Manual/Automatic Layout Generation and Post Layout Extraction for experiments 7 to 9

Analyze the power, area and timing for experiments 7 to 9 by performing Pre Layoutand Post Layout Simulations.

Part-III Analog Circuit Design (12 Periods)

10. Design and Simulate a CMOS Inverting Amplifier.
11. Design and Simulate basic Common Source, Common Gate and Common DrainAmplifiers.

Analyze the input impedance, output impedance, gain and bandwidth for experiments 10 and 11 by performing Schematic Simulations.

Design and simulate simple 5 transistor differential amplifier. Analyze Gain,

12. Bandwidth and CMRR by performing Schematic Simulations.

Requirements: Cadence/Synopsis/ Mentor Graphics/Tanner/equivalent EDA Tools

EC8711 EMBEDDED LABORATORY

1. Study of ARM evaluation system
2. Interfacing ADC and DAC.
3. Interfacing LED and PWM.
4. Interfacing real time clock and serial port.
5. Interfacing keyboard and LCD.
6. Interfacing EPROM and interrupt.
7. Mailbox.
8. Interrupt performance characteristics of ARM and FPGA.
9. Flashing of LEDS.
10. Interfacing stepper motor and temperature sensor.
11. Implementing zigbee protocol with ARM.

EC8761 ADVANCED COMMUNICATION LABORATORY

LIST OF OPTICAL EXPERIMENTS

1. Measurement of connector, bending and fiber attenuation losses.
2. Numerical Aperture and Mode Characteristics of Fibers.
3. DC Characteristics of LED and PIN Photo diode.
4. Fiber optic Analog and Digital Link Characterization - frequency response(analog), eye diagramand BER (digital)

LIST OF WIRELESS COMMUNICATION EXPERIMENTS

1. Wireless Channel Simulation including fading and Doppler effects
2. Simulation of Channel Estimation, Synchronization & Equalization techniques
3. Analysing Impact of Pulse Shaping and Matched Filtering using Software Defined Radios
4. OFDM Signal Transmission and Reception using Software Defined Radios

LIST OF MICROWAVE EXPERIMENTS

1. VSWR and Impedance Measurement and Impedance Matching
2. Characterization of Directional Couplers, Isolators, Circulators
3. Gunn Diode Characteristics
4. Microwave IC – Filter Characteristics

Information Technology

IT8211 Information Technology Essentials Laboratory

1. Creation of interactive web sites - Design using HTML and authoring tools
2. Creation of simple PHP scripts - Dynamism in web sites
3. Handling multimedia content in web sites
4. Database applications using PHP and MySQL
5. Study of computer networking components
6. Creation of information retrieval system using web, PHP and MySQL
7. Study of Technologies associated with mobile devices
8. Creation of Personal Information System

CS8261 C Programming Laboratory

1. Programs using I/O statements and expressions.
2. Programs using decision-making constructs.
3. Write a program to find whether the given year is leap year or Not? (Hint: not every centurionyear is a leap. For example 1700, 1800 and 1900 is not a leap year)
4. Design a calculator to perform the operations, namely, addition, subtraction, multiplication,division and square of a number.
5. Check whether a given number is Armstrong number or not?
6. Given a set of numbers like <10, 36, 54, 89, 12, 27>, find sum of weights based on

thefollowing conditions

- 5 if it is a perfect cube
- 4 if it is a multiple of 4 and divisible by 6
- 3 if it is a prime number

Sort the numbers based on the weight in the increasing order as shown below

<10,its weight>,<36,its weight><89,its weight>

7. Populate an array with height of persons and find how many persons are above the averageheight.
8. Populate a two dimensional array with height and weight of persons and compute the BodyMass Index of the individuals.
9. Given a string -a$bcd./fg‖ find its reverse without changing the position of special characters. (Example input:a@gh%;j and output:j@hg%;a)
10. Convert the given decimal number into binary, octal and hexadecimal numbers using userdefined functions.
11. From a given paragraph perform the following using built-in functions:
    a. Find the total number of words.
    b. Capitalize the first word of each sentence.
    c. Replace a given word with another word.
12. Solve towers of Hanoi using recursion.
13. Sort the list of numbers using pass by reference.
14. Generate salary slip of employees using structures and pointers.
15. Compute internal marks of students for five different subjects using structures and functions.
16. Insert, update, delete and append telephone details of an individual or a company into atelephone directory using random access file.
17. Count the number of account holders whose balance is less than the minimum balance usingsequential access file.

Mini Project

18. Create a -Railway reservation system‖ with the following modules
    - Booking
    - Availability checking
    - Cancellation
    - Prepare chart

CS8382 DIGITAL SYSTEMS LABORATORY

1. Verification of Boolean Theorems using basic gates.
2. Design and implementation of combinational circuits using basic gates for arbitrary

functions, code converters.

3. Design and implement Half/Full Adder and Subtractor.
4. Design and implement combinational circuits using MSI devices:
    - 4 – bit binary adder / subtractor
    - Parity generator / checker
    - Magnitude Comparator
    - Application using multiplexers
5. Design and implement shift-registers.
6. Design and implement synchronous counters.
7. Design and implement asynchronous counters.
8. Coding combinational circuits using HDL.
9. Coding sequential circuits using HDL.
10. Design and implementation of a simple digital system (Mini Project).

CS8381 DATA STRUCTURES LABORATORY

1. Array implementation of Stack and Queue ADTs
2. Array implementation of List ADT
3. Linked list implementation of List, Stack and Queue ADTs
4. Applications of List, Stack and Queue ADTs
5. Implementation of Binary Trees and operations of Binary Trees
6. Implementation of Binary Search Trees
7. Implementation of AVL Trees
8. Implementation of Heaps using Priority Queues.
9. Graph representation and Traversal algorithms
10. Applications of Graphs
11. Implementation of searching and sorting algorithms
12. Hashing – any two collision techniques

CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY

1. Develop a Java application to generate Electricity bill. Create a class with the followingmembers:
   Consumer no., consumer name, previous month reading, current month reading,

   type of EB connection(i.e domestic or commercial). Compute the bill amount using thefollowing tariff.

If the type of the EB connection is domestic, calculate the amount to be paid as follows:

- First 100 units - Rs. 1 per unit
- 101-200 units - Rs. 2.50 per unit
- 201 -500 units - Rs. 4 per unit
- > 501 units - Rs. 6 per unit

If the type of the EB connection is commercial, calculate the amount to be paid as follows:

- First 100 units - Rs. 2 per unit
- 101-200 units - Rs. 4.50 per unit
- 201 -500 units - Rs. 6 per unit
- > 501 units - Rs. 7 per unit

2. Develop a java application to implement currency converter (Dollar to INR, EURO to INR, Yento INR and vice versa), distance converter (meter to KM, miles to KM and vice versa) , time converter (hours to minutes, seconds and vice versa) using packages.

3. Develop a java application with Employee class with Emp_name, Emp_id, Address, Mail_id, Mobile_no as members. Inherit the classes, Programmer, Assistant Professor, Associate Professor and Professor from employee class. Add Basic Pay (BP) as the member of all the inherited classes with 97% of BP as DA, 10 % of BP as HRA, 12% of BP as PF, 0.1% of BP for staff club fund. Generate pay slips for the employees with their gross and net salary.

4. Design a Java interface for ADT Stack. Implement this interface using array. Provide necessary exception handling in both the implementations.

5. Write a program to perform string operations using Array List. Write functions for the following

   a. Append - add at end

   b. Insert – add at particular index

   c. Search

   d. List all string starts with given letter

6. Write a Java Program to create an abstract class named Shape that contains two integers and an empty method named print Area(). Provide three classes named Rectangle, Triangle and Circle such that each one of the classes extends the class Shape. Each one of the classes contains only the method print Area () that prints the area of the given shape.

7. Write a Java program to implement user defined exception handling.

8. Write a Java program that reads a file name from the user, displays information about whether the file exists, whether the file is readable, or writable, the type of file and the length of the filein bytes.

9. Write a java program that implements a multi-threaded application that has three threads. First thread generates a random integer every 1 second and if the value is even, second thread computes the square of the number and prints. If the value is odd, the third thread will print the value of cube of the number.

10. Write a java program to find the maximum value from the given type of elements using ageneric function.

11. Design a calculator using event-driven programming paradigm of Java with the followingoptions.

    a) Decimal manipulations

    b) Scientific manipulations

12. Develop a mini project for any application using Java concepts.

CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY

1. Data Definition Commands, Data Manipulation Commands for inserting, deleting, updating and retrieving Tables and Transaction Control statements
2. Database Querying – Simple queries, Nested queries, Sub queries and Joins
3. Views, Sequences, Synonyms
4. Database Programming: Implicit and Explicit Cursors
5. Procedures and Functions
6. Triggers
7. Exception Handling
8. Database Design using ER modeling, normalization and Implementation for any application
9. Database Connectivity with Front End Tools
10. Case Study using real life database applications

CS8461 OPERATING SYSTEMS LABORATORY

1. Basics of UNIX commands
2. Write programs using the following system calls of UNIX operating systemfork, exec, getpid, exit, wait, close, stat, opendir, readdir
3. Write C programs to simulate UNIX commands like cp, ls, grep, etc.
4. Shell Programming
5. Write C programs to implement the various CPU Scheduling Algorithms
6. Implementation of Semaphores

7. Implementation of Shared memory and IPC
8. Bankers Algorithm for Deadlock Avoidance
9. Implementation of Deadlock Detection Algorithm
10. Write C program to implement Threading & Synchronization Applications
11. Implementation of the following Memory Allocation Methods for fixed partition
    a) First Fit b) Worst Fit c) Best Fit
12. Implementation of Paging Technique of Memory Management
13. Implementation of the following Page Replacement Algorithms
    a) FIFO b) LRU c) LFU
14. Implementation of the various File Organization Techniques
15. Implementation of the following File Allocation Strategies
    a) Sequential b) Indexed c) Linked

EC8681 MICROPROCESSOR AND MICROCONTROLLER LABORATORY

8086 Programs using kits and MASM

1. Basic arithmetic and Logical operations
2. Move a data block without overlap
3. Code conversion, decimal arithmetic and Matrix operations.
4. Floating point operations, string manipulations, sorting and searching
5. Password checking, Print RAM size and system date
6. Counters and Time Delay

Peripherals and Interfacing Experiments

7. Traffic light controller
8. Stepper motor control
9. Digital clock
10. Key board and Display
11. Printer status
12. Serial interface and Parallel interface
13. A/D and D/A interface and Waveform Generation
14. 8051 Experiments using kits and MASM
15. Basic arithmetic and Logical operations
16. Square and Cube program, Find 2‘s complement of a number
17. Unpacked BCD to ASCII

CS8581 NETWORKS LABORATORY

1.Learn to use commands like tcpdump, netstat, ifconfig, nslookup and traceroute. Capture ping and traceroute PDUs using a network protocol analyzer and examine.

2.Write a HTTP web client program to download a web page using TCP sockets.

3.Applications using TCP sockets like:

- Echo client and echo server
- Chat
- File Transfer

4.Simulation of DNS using UDP sockets.

5.Write a code simulating ARP /RARP protocols.

6.Study of Network simulator (NS) and Simulation of Congestion Control Algorithms usingNS.

7.Study of TCP/UDP performance using Simulation tool.

8.Simulation of Distance Vector/ Link State Routing algorithm.

9.Performance evaluation of Routing protocols using Simulation tool.

10.Simulation of error correction code (like CRC).

IT8511 WEB TECHNOLOGY LABORATORY

1. Create a web page with the following using HTML.
   i) To embed an image map in a web page.
   ii) To fix the hot spots.
   iii) Show all the related information when the hot spots are clicked
2. Create a web page with all types of Cascading style sheets.
3. Client Side Scripts for Validating Web Form Controls using DHTML.
4. Installation of Apache Tomcat web server.
5. Write programs in Java using
   Servlets: To invoke servlets from
   HTML forms.Session Tracking.
6. Write programs in Java to create three-tier applications using JSP and Databases
   - For conducting on-line examination.
   - For displaying student mark list. Assume that student information is available in a database which has been stored in a database server.
7. Programs using XML – Schema – XSLT/XSL.
8. Programs using DOM and SAX parsers.
9. Programs using AJAX.
10. Consider a case where we have two web Services- an airline service and a travel agent and the travel agent is searching for an airline. Implement this scenario using Web Services and Data base.

CS8582 OBJECT ORIENTED ANALYSIS AND DESIGN LABORATORY

Draw standard UML diagrams using an UML modeling tool for a given case study and map design to code and implement a 3 layered architecture. Test the developed code and validate whether the SRSis satisfied.

1. Identify a software system that needs to be developed.
2. Document the Software Requirements Specification (SRS) for the identified systemIdentify use cases and develop the Use Case model.
3. Identify the conceptual classes and develop a Domain Model and also derive a Class Diagram from that.
4. Using the identified scenarios, find the interaction between objects and represent them usingUML Sequence and Collaboration Diagrams
5. Draw relevant State Chart and Activity Diagrams for the same system.
6. Implement the system as per the detailed design
7. Test the software system for all the scenarios identified as per the usecase diagram
8. Improve the reusability and maintainability of the software system by applying appropriatedesign patterns.
9. Implement the modified system and test it for various scenarios

SUGGESTED DOMAINS FOR MINI-PROJECT:

1. Passport automation system.
2. Book bank
3. Exam registration
4. Stock maintenance system.
5. Online course reservation system
6. Airline/Railway reservation system
7. Software personnel management system
8. Credit card processing
9. e-book management system
10. Recruitment system
11. Foreign trading system
12. Conference management system
13. BPO management system
14. Library management system
15. Student information system

CS8662 MOBILE APPLICATION DEVELOPMENT LABORATORY

1. Develop an application that uses GUI components, Font and Colours
2. Develop an application that uses Layout Managers and event listeners.
3. Write an application that draws basic graphical primitives on the screen.

4. Develop an application that makes use of databases.
5. Develop an application that makes use of Notification Manager
6. Implement an application that uses Multi-threading
7. Develop a native application that uses GPS location information
8. Implement an application that writes data to the SD card.
9. Implement an application that creates an alert upon receiving a message
10. Write a mobile application that makes use of RSS feed
11. Develop a mobile application to send an email.
12. Develop a Mobile application for simple needs (Mini Project)

IT8761 SECURITY LABORATORY

1. Perform encryption, decryption using the following substitution techniques
2. (i) Ceaser cipher, (ii) playfair cipher iii) Hill Cipher iv) Vigenere cipher
3. Perform encryption and decryption using following transposition techniques
4. i)Rail fence ii) row & Column Transformation
5. Apply DES algorithm for practical applications.
6. Apply AES algorithm for practical applications.
7. Implement RSA Algorithm using HTML and JavaScript
8. Implement the Diffie-Hellman Key Exchange algorithm for a given problem.
9. Calculate the message digest of a text using the SHA-1 algorithm.
10. Implement the SIGNATURE SCHEME - Digital Signature Standard.
11. Demonstrate intrusion detection system (ids) using any tool eg. Snort or any other s/w.
12. Automated Attack and Penetration Tools
    i. Exploring N-Stalker, a Vulnerability Assessment Tool
13. Defeating Malware
    i. i) Building Trojans ii) Rootkit Hunter

IT8711 FOSS AND CLOUD COMPUTING LABORATORY

1. Use gcc to compile c-programs. Split the programs to different modules and create anapplication using make command.
2. Use version control systems command to clone, commit, push, fetch, pull, checkout, reset, anddelete repositories.
3. Install Virtualbox/VMware Workstation with different flavours of linux or windows OS on top ofwindows7 or 8.
4. Install a C compiler in the virtual machine created using virtual box and execute SimplePrograms
5. Install Google App Engine. Create *hello world* app and other simple web applications usingpython/java.

6. Use GAE launcher to launch the web applications.
7. Simulate a cloud scenario using CloudSim and run a scheduling algorithm that is not presentin CloudSim.
8. Find a procedure to transfer the files from one virtual machine to another virtual machine.
9. Find a procedure to launch virtual machine using trystack (Online Openstack Demo Version)
10. Install Hadoop single node cluster and run simple applications like wordcount

Computer Science and Business Systems

GE8161 PROBLEM SOLVING AND PYTHON PROGRAMMING LABORATORY

1. Compute the GCD of two numbers.
2. Find the square root of a number (Newton's method)
3. Exponentiation (power of a number)
4. Find the maximum of a list of numbers
5. Linear search and Binary search
6. Selection sort, Insertion sort
7. Merge sort
8. First n prime numbers
9. Multiply matrices
10. Programs that take command line arguments (word count)
11. Find the most frequent words in a text read from a file
12. Simulate elliptical orbits in Pygame
13. Simulate bouncing ball using Pygame

AD8261 DATA STRUCTURES DESIGN LABORATORY

1. Implement simple ADTs as Python classes
2. Implement recursive algorithms in Python
3. Implement List ADT using Python arrays

4. Linked list implementations of List
5. Implementation of Stack and Queue ADTs
6. Applications of List, Stack and Queue ADTs
7. Implementation of sorting and searching algorithms
8. Implementation of Hash tables
9. Tree representation and traversal algorithms
10. Implementation of Binary Search Trees
11. Implementation of Heaps
12. Graph representation and Traversal algorithms
13. Implementation of single source shortest path algorithm
14. Implementation of minimum spanning tree algorithms

CW8311 BUSINESS COMMUNICATION AND VALUE SCIENCE LAB-1

1. Different forms of words
2. Technical terminology
3. Interpersonal Skills: Dialogue & Conversation
4. Job Application
5. Letters & Reports
6. SWOT analysis
7. Socio cultural & Cross-cultural understanding
8. Women in all spheres
9. Team vs Group
10. Conflict management
11. Acquiring Leadership traits
12. Human values and Corporate culture

CS8383 OBJECT ORIENTED PROGRAMMING LABORATORY

1. Develop a Java application to generate Electricity bill. Create a class with the following members: Consumer no., consumer name, previous month reading, current month reading, type of EB connection (i.e domestic or commercial). Compute the bill amount using the following tariff.

If the type of the EB connection is domestic, calculate the amount to be paid as follows:
□□First 100 units - Rs. 1 per unit

□□101-200 units - Rs. 2.50 per unit

□□201 -500 units - Rs. 4 per unit

□□> 501 units - Rs. 6 per unit

If the type of the EB connection is commercial, calculate the amount to be paid as follows:
□□First 100 units - Rs. 2 per unit

□□101-200 units - Rs. 4.50 per unit

□□201 -500 units - Rs. 6 per unit

□□> 501 units - Rs. 7 per unit

2. Develop a java application to implement currency converter (Dollar to INR, EURO to INR, Yen to INR and vice versa), distance converter (meter to KM, miles to KM and vice versa) , time conerter (hours to minutes, seconds and vice versa) using packages.

3. Develop a java application with Employee class with Emp_name, Emp_id, Address, Mail_id, Mobile_no as members. Inherit the classes, Programmer, Assistant Professor, Associate Professor and Professor from employee class. Add Basic Pay (BP) as the member of all the inherited classes with 97% of BP as DA, 10 % of BP as HRA, 12% of BP as PF, 0.1% of BP for staff club fund. Generate pay slips for the employees with their gross and net salary.

4. Design a Java interface for ADT Stack. Implement this interface using array. Provide

necessary exception handling in both the implementations

5. Write a program to perform string operations using ArrayList. Write functions for the following

a. Append - add at end
b. Insert – add at particular index
c. Search
d. List all string starts with given letter

6. Write a Java Program to create an abstract class named Shape that contains two integers and an empty method named print Area(). Provide three classes named Rectangle, Triangle and Circle such that each one of the classes extends the class Shape. Each one of the classes contains only the method print Area () that prints the area of the given shape.

7. Write a Java program to implement user defined exception handling.

8. Write a Java program that reads a file name from the user, displays information about whether the file exists, whether the file is readable, or writable, the type of file and the length of the file in bytes.

9. Write a java program that implements a multi-threaded application that has three threads. First thread generates a random integer every 1 second and if the value is even, second thread computes the square of the number and prints. If the value is odd, the third thread will print the value of cube of the number.

10. Write a java program to find the maximum value from the given type of elements using a generic function.

11. Design a calculator using event-driven programming paradigm of Java with the following options.

a) Decimal manipulations
b) Scientific manipulations

12. Develop a mini project for any application using Java concepts.

CS8481 DATABASE MANAGEMENT SYSTEMS LABORATORY

1. Data Definition Commands, Data Manipulation Commands for inserting, deleting, updating and retrieving Tables and Transaction Control statements

2. Database Querying – Simple queries, Nested queries, Sub queries and Joins

3. Views, Sequences, Synonyms

4. Database Programming: Implicit and Explicit Cursors

5. Procedures and Functions

6. Triggers

7. Exception Handling

8. Database Design using ER modeling, normalization and Implementation for any application

9. Database Connectivity with Front End Tools

10. Case Study using real life database applications

CS8461 OPERATING SYSTEMS LABORATORY

1. Basics of UNIX commands

2. Write programs using the following system calls of UNIX operating system
fork, exec, getpid, exit, wait, close, stat, opendir, readdir

3. Write C programs to simulate UNIX commands like cp, ls, grep, etc.

4. Shell Programming

5. Write C programs to implement the various CPU Scheduling Algorithms

6. Implementation of Semaphores

7. Implementation of Shared memory and IPC

8. Bankers Algorithm for Deadlock Avoidance

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10. Write C program to implement Threading & Synchronization Applications

11. Implementation of the following Memory Allocation Methods for fixed partition

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12. Implementation of Paging Technique of Memory Management

13. Implementation of the following Page Replacement Algorithms

a) FIFO b) LRU c) LFU

14. Implementation of the various File Organization Techniques

15. Implementation of the following File Allocation Strategies

a) Sequential b) Indexed c) Linked

CW8411 COMPUTATIONAL STATISTICS LABORATORY

1. Install R and R Studio
2. Creation and manipulation of Vectors, Matrices, Arrays, Lists, Factors and Data Frames
3. Install of Packages and scripts for Importing and Exporting Data
4. Implement Control structures and Functions
5. Visualize Statistical Graphs using Scatter Plots, Box Plots, Whisker Plot, Histograms
6. Perform Data exploration and visualization techniques over a dataset.
7. Perform Data Query using SQL and R.
8. Create a data set and do statistical analysis on the data

Artificial Intelligence and Machine Learning

21GE111 C PROGRAMMING LABORATORY

1. Constructing flow charts using RAPTOR tools.
2. Programs using I/O statements and expression
3. Write a program to find whether the given line is horizontal or vertical.
4. Write a program to calculate the distance between two points p1(x1,y1), p2(x2,y2).
5. Write a program to calculate the force for the given mass and acceleration.
6. Write a program to calculate the Young's modulus.
7. Write a program to calculate the type of solution based on its pH value.
8. Write a program to temperature conversion (Fahrenheit to Celsius and vice versa)
9. Programs using decision-making constructs.
10. Write a program to find whether the given year is leap year or Not? (Hint: not everycenturion year is a leap. For example 1700, 1800 and 1900 is not a leap year)
11. Design a calculator to perform the operations, namely, addition,subtraction, multiplication, division and square of a number.
12. Check whether a given number is Armstrong number or not?
13. Given a set of numbers like, find sum of weights based on the followingconditions.

- 5 if it is a perfect cube.
- 4 if it is a multiple of 4 and divisible by 6.
- 3 if it is a prime number.

Sort the numbers based on the weight in the increasing order as shown-below

<10,its weight>, <36,its weight>, <89,its weight>

14. Populate an array with height of persons and find how many persons areabove the average height.
15. Populate a two dimensional array with height and weight of persons andcompute the Body Mass Index of the individuals.Given a string —a$bcd./fg find its reverse without changing the position of specialcharacters.(Example input:a@gh%;j and output:j@hg%;a)
16. Convert the given decimal number into binary, octal and hexadecimal numbersusing user defined functions.
17. From a given paragraph perform the following using built-in functions:
    a. Find the total number of words.
    b. Capitalize the first word of each sentence.
    c. Replace a given word with another word.
18. Solve towers of Hanoi using recursion.
19. Sort the list of numbers using pass by reference.
20. Generate salary slip of employees using structures and pointers. Create a structure Employee with the following members: EID, Ename, Designation, DOB, DOJ, Basic pay Note that DOB and DOJ should be implemented using structure within structure.
21. Compute internal marks of students for five different subjects using structures andfunctions.
22. Insert, update, delete and append telephone details of an individual or a company into a telephone directory using random access file.
23. Count the number of account holders whose balance is less than the minimum balance using sequential access file.
24. Mini project: Create a —Railway reservation system with the following modules

- Booking
- Availability checking
- Cancellation
- Prepare chart

21CS211 DATA STRUCTURES LABORATORY

1. Array Manipulation
    a. Find kth smallest element in an unsorted array
    b. Find the sub array with given sum
    c. Matrix manipulations – Addition, Subtraction, Multiplication
    d. Job Sequencing: Given an array of jobs where every job has a deadline and a profit. Profit can be earned only if the job is finished before the deadline. It is alsogiven that every job takes a single unit of time, so the minimum possible deadlinefor any job is 1. How to maximize total profit if only one job can be scheduled ata time. Print the sequence of jobID order to maximize total profit.
2. String manipulations:
    a. Reversing a set of words and count the frequency of each letter in the string.
    b. Pattern Recognition - Find the number of patterns of form 1[0]1 where [0] representsany number of zeroes (minimum requirement is one 0) there should not be any other character except 0 in the [0] sequence in a given binary string.
    c. Remove all the occurrences of string S2 in string S1 and print the remaining.
3. Pointers
    a. Manipulating two dimensional arrays using pointers.
    b. Print all permutations of a given string using pointers.
        4. Dynamic Memory Allocation
            a. Find Largest Number.
    b. Print the list in reverse order.
5. Array implementation of List, Stack and Queue ADTs.
6. Linked list implementation of List, Stack and Queue ADTs.
7. Applications of List, Stack and Queue ADTs.
8. Implementation of Binary Trees and operations of Binary Trees.
9. Implementation of Binary Search Trees.
10. Implementation of AVL Trees.
11. Implementation of Heaps using Priority Queues.
12. Graph representation and Traversal algorithms.
13. Implement searching and sorting algorithms. Analyze and compare the time taken forvarious algorithms with best, average and worst case inputs.

Innovation Cell

RMDEC IIC objective is to create a vibrant innovation ecosystem and Start-up supporting Mechanism and inculcate Ideas and Pre-incubation of Ideas. Develop better Cognitive Ability for RMDEC Engineering Students. To Prepare RMDEC for better for Atal Ranking

RMDEC IIC is headed by Principal as President and council of Staff members and student members in the areas of Startup ,IPR,NIRF,ARIIA,Innovation,Incubation

Functions of RMDEC IICs

- *To conduct various innovation and entrepreneurship-related activities prescribed by Central MIC in time bound fashion.*
- *Identify and reward innovations and share success stories.*
- *Organize periodic workshops/ seminars/ interactions with entrepreneurs, investors, professionals and create a mentor pool for student innovators.*
- *Network with peers and national entrepreneurship development organizations.*
- *Organize Hackathons, idea competition, mini-challenges etc. with the involvement of industries.*

Social Media Cell

R.M.D. Engineering College has established an official presence on Facebook, Instagram, LinkedIn and Twitter.These social media accounts are all maintained by the Social Media Committee of the college.

With the majority of student crowd present on social media, it provides us a platform to promote activities, receive feedback and start conversations. It provides a better way to connect with parents and the community and keep them up to date. This is a very effective tool for Alumni Engagement. Thus, Connecting Students, Teachers, Parents, Alumni and other stakeholders, social media plays an important medium of communication.

Below is the link for the social media accounts.

Facebook:

https://www.facebook.com/rmdecprincipal

Instagram:

https://www.instagram.com/rmdecprincipal

LinkedIn:

https://www.linkedin.com/in/rmdecprincipal

Twitter:

https://twitter.com/rmdecprincipal

YouTube:

https://www.youtube.com/RMDEnggCollege

Short video (1-2 min) of Infrastructure and facilities available

https://drive.google.com/file/d/1o8FQ76hHXLpGOE6H1xyHFAb4nOuuc7bw/view?usp=sharing

List of Facilities available

Games and Sports Facilities

- Multi Gym
- Cricket Ground
- Athletics Ground
- Volleyball Court – I
- Volleyball Court – II
- Basketball Court
- Tennis Court - I
- Futsal Court
- Badminton Court - I
- Badminton Court – II
- Badminton Court - III
- Tennis Court – I
- Volleyball Court – III
- Volleyball Court – IV
- Badminton Court - IV
- Badminton Court – V
- Volleyball Court - V
- Volleyball Court - VI
- Ball Badminton Court – I

List of sports Equipment

S.No	Name of the equipment	Numbers
1	Ball Badminton Ball	20 Nos
2	Ball Badminton Bat	20 Nos
3	Ball Badminton Net	01 No
4	Basket Ball	08 Nos
5	Basket Ball Board	01 Set
6	Basket ball Net	02 Nos
7	Carrom Board & coins	10 Box
8	Carrom Board Striker	10 Nos
9	Chess Board	10 Nos
10	Chess Clock	04 Nos
11	Cone	35 Nos
12	Cricket Abdomen Guard	12 Nos
13	Cricket Arm Guard	06 Nos
14	Cricket Balls (cork ball)	25 Nos
15	Cricket Bat - English willow	08 Nos
16	Cricket Bat - Kashmir willow	06 Nos
17	Cricket Batting Gloves	08 Pairs
18	Cricket Batting Inner Gloves	08 Pairs
19	Cricket Batting pads	08 Pairs
20	Cricket Helmet	06 Nos
21	Cricket Mat	02 Nos
22	Cricket Kit Bag	04 Nos
23	Cricket seasoning hammer	02 Nos
24	Cricket Stumps	20 Nos
25	Cricket Thigh Pads	06 Nos
26	Cricket W. Keeper Gloves	04 Pairs
27	Cricket W. Keeper Inner Gloves	04 Pairs
28	Cricket W. Keeper Pads	02 Sets
29	Discuss 1 KG (Nelco)Women	02 Nos
30	Discuss 2 KG (Nelco)Men	02 Nos
31	Foot Ball	08 Nos
32	Foot Ball Gloves	03 Nos
33	Foot ball Post	01 Pair
34	Foot ball net	01 No
35	Foot Pump	01 Nos

S.No	Name of the equipment	Numbers
36	Hammer throw - Men	01 No
37	Hammer throw - Women	01 No
38	Hand ball	04 Nos
39	Hand ball Glove	02 Nos
40	Hand ball Post	01 Pair
41	Hand ball Net	01 No
42	Hurdles	12 Nos
43	Javelin (600 gm) Nelco	02 Nos
44	Javelin (800 gm) Nelco	02 Nos
45	Measuring Tapes	01 No
46	Medicine ball	05 Nos
47	Racer Stop Watch	02 Nos
48	Relay Baton Nalco	08 Nos
49	Shot put 4 kg Nelco	01 No
50	Shot Put 7.26 kg Nelco	01 No
51	Shuttle Cock feather	02 Box
52	Shuttle Net	01 No
53	Shuttle Post Iron	02 Nos
54	Shuttle Racket	05 Nos
55	Skipping Rope	06 Nos
56	Table Tennis Ball	20 Nos
57	Table Tennis Bat	10 Nos
58	Table Tennis Board	01 No
59	Table Tennis Net	01 No
60	Tennikoit ring ball	04 Nos
61	Tennis Ball	06 Nos
62	Tennis Ball for Cricket	20 Nos
63	Tennis Iron Rope	02 Nos
64	Tennis Net	02 Nos
65	Tennis Post	04 Nos
66	Tennis Racket	06 Nos
67	Throw Ball	03 Nos
68	Volley Ball (Cosco)	05 Nos
69	Volley Ball Net (Iron Rope)	04 Nos
70	Volley Ball Net (wire)	04 Nos
71	Volley Ball Post	06 Nos

S.No	Name of the equipment	Numbers
72	Weighing Machine	01 No
73	Whistle fox 40	03 Nos

GYMNASIUM

MULTI - GYM

We have well equipped power gym and hydraulic gym (Air Conditioned) with latest multiple machines. These gyms could accommodate 100 Students per session. Each session isengaged by experienced trainers.

Teaching Learning Process

Internal Continuous Evaluation System and place

Academic calendar is prepared matching the prescribed number of working days specified by Anna University. Academic calendar contains total number of working days month wise, schedule of University theory and practical examination, schedule for two internal evaluation tests for each of the courses and model examination and symposium schedule. The academic calendar is prepared by the academic co-ordinator in consultation with the heads of the departments and due approval of the principal. The institution strictly adheres to the academic calendar including for the conduct of continuous internal evaluation (CIE).

The academic calendar is issued well in advance of the ensuing semester to all the department faculty members and students. This enables effective planning by the faculty for each course and all curricular, co-curricular and extracurricular activities. Based on the academic calendar, lesson plan is prepared by the course coordinator, which includes unit wise topics with content beyond the syllabus, teaching aids used for effective course delivery.

A committee nominated by the head of the department of each programme frames academic timetable adhering to

the academic calendar to ensure effective curriculum delivery both theory and laboratory courses.

The institution academic calendar provides the information and scheduled time table for continuous internal evaluation (CIE) and model examinations to enable the students to frame their action plan for the entire semester.

Continuous Internal Evaluation process (CIE)

The subject wise Continuous Internal Evaluation process (CIE) timetable matching with the schedule given in the academic calendar is announced to the students one week prior to the commencement of the internal evaluation test schedule.

Continuous internal evaluation (CIE) in theory courses:

Table 1: Evaluation Tools and content

Evaluation Tools	**content**	**Marks**	Duration
Internal Assessment I	Units I and II	100	Three Hours
Internal Assessment II	Units III and IV	100	
Model Examination	All Five Units	100	

- The Internal evaluation tests are conducted after the completion of every 2 units. The subject wise Internal evaluation test schedule and staff invigilation schedule for the test are prepared by the Exam-cell and circulated to all the departments.

Continuous internal evaluation (CIE) in laboratory courses:

- Regular monitoring of the student's performance and ability to conduct the experiment during lab sessions.
- Evaluation of the student's observation book and record notebook.
- Evaluation by oral examination on the student's knowledge of the experiment
- Model practical examination.
- Internal mark is awarded to each student based on performance in the evaluation process.

Teaching Learning Process

The primary focus at RMDEC is to move away from lecture-based classroom to more interactive teaching facilitated using ICT enabled tools. The teaching-learning strategy adopted is to create opportunity for multidisciplinary activities and for students' interaction with teachers, with each other, and with the industry. The objective is to embrace more interactive teaching and create a more supportive and challenging classroom setting for the students to learn actively.

For efficient implementation of interactive teaching techniques, the faculty are encouraged to use ICT enabled tools such as digital and online technology. Power-point presentations and animations, smart classrooms, interactive white-boards, guided web tours, learning Online resources, virtual labs, educational interactive audio-video chats, webinars, Course material and YouTube videos posted by the faculty of RMDEC, etc. are regularly used by the faculty.

The students are constantly encouraged to access E-Resources such as E-Books, digital libraries, online free simulation tools, and MOOC (massive open online courses such as NPTEL). The college provides facilities to use online platforms such as Skill Rack, AMCAT for the students to improve their programming skills. Quiz questions are also posted by the faculty on Skill Rack platform, for the students to practice online.

The teaching resource material and YouTube videos published online by the faculty of RMDEC has transformed the teaching- learning process from confined classroom delivery to open, flexible delivery for the benefit of students to learn independently by adopting ICT enabled tools for effective teaching-learning. The necessary links for the teaching resources and YouTube videos are provided on the college website.

The use of ICT tools for teaching-learning at RMDEC provides an affable atmosphere, inspiration, and impact on the process of learning.

Institution provides complete infrastructure for ICT enabled teaching-learning processes and for content development by the faculty. Facilities such as seminar halls, smart classrooms, equipped with smart boards, LCD projectors, video cameras, Internet, and Wi-Fi etc.

Mechanism of internal assessment is transparent and robust in terms of frequency and mode.

Robust and Transparent system of internal assessment, the frequency and mode are very clearly stipulated in Anna University regulation 2017 (section 11 & 12, Page 8,9) and it is mandatory for the college to follow the system. The regulation is published for transparency on the University website under the head Academic, for public viewing.

Transparency initiatives at college level

RMDEC on its part, to ensure awareness and transparency among students conducts special orientation programme on the examination system both university and internal assessment and award of internal marks, frequency and mode to all the first-year students at entry level itself.

The controller of examinations Anna University notifies the academic schedule for each semester to the college. Based on this schedule, an academic calendar is prepared by the college which includes schedules for two Internal Assessment Tests, Model Examination, and tentative date of commencement of university examinations. Hard copy of the academic calendar is provided to each student and faculty of the college to ensure transparency. The Internal Assessment Tests and Model Examination are conducted as scheduled in the academic calendar.

The timetable for the Assessment test is prepared by the Exam cell and displayed on the department Notice Board one week in advance. It is also circulated to students. Proper Seating plan is followed for internal assessment tests, and it is displayed on the notice board as well as on the respective classrooms. Changes in schedules, patterns, methods if any, are immediately notified to the students through notice boards and through classroom briefing by the concerned subject teacher.

Two internal assessment tests and one model exam are conducted per semester. Exam cell allocates experienced faculty as overall course coordinator. Detailed instructions about the format of the question paper are given by notification from the principal.

SMART PICK SYSTEM OF QUESTION PAPER SETTING:

Question bank is prepared for each course by an experienced course coordinator nominated for each course by extracting and consolidating the questions prepared by different faculty handling the same course. The question bank covers Part – A, Part – B and Part C questions. The final question paper is generated using Smart Pick Soft Ware to assures transparency.

Internal assessment test answer papers are evaluated by the concerned course teachers whereas model exam answer papers are shuffled and evaluated by different teachers handling the same course thereby eliminating any bias. It is the practice of the college, to give the internal examination answer books after evaluation, to the students in the class for self-evaluation.

The Internal Assessment marks are entered periodically in Anna University Web Portal in four scheduled phases in a semester specified by the University. The students can view their internal assessment marks in the web portal through Anna University Student login.

Thus the mechanism of internal assessment system followed by the college guarantees transparency and is robust in terms of frequency and mode.

Student's assessment of Faculty, System in place

A class committee comprises of six student representatives, a faculty as Chairperson; branch coordinator, all faculty handling courses for the class & the Head of the Department as the special invitee. The committee is convened thrice in a semester. Quality of content delivery by faculty, syllabus completion etc are discussed in the committee as well as any welfare points raised by the students are recorded and appropriate remedial actions are initiated. Online feedback system is used to assess the effectiveness of curriculum delivery and are documented.

16. Enrollment and placement details of the Students in last 3 years

Enrollment

Dept	Academic year 2024-25				Academic year 2023-24				Academic year 2022-23			
	Sanctioned	Admitted			Sanctioned	Admitted			Sanctioned	Admitted		
		CAT-I	CAT-II	Total		CAT-I	CAT-II	Total		CAT-I	CAT-II	Total
CSE	240	109	143	252	240	115	134	249	180	83	104	187
ECE	180	86	102	188	180	83	103	186	180	79	104	183
IT	120	58	64	122	60	28	35	63	60	29	34	63
CSBS	60	28	32	60	60	27	36	63	60	20	40	60
AIML	60	28	35	63	60	27	36	63	60	28	33	61
TOTAL	660	309	376	685	600	280	344	624	**540**	**239**	**315**	**554**

Campus placement in last three years with minimum salary, maximum salary and average salary

Batch	No. of Students Eligible	No. of Students Placed	Placement %	Min. Salary (Lakhs)	Max. Salary (Lakhs)	Avg. Salary (Lakhs)
2021-25	424	303	71.46	2.2 LAKHS	9.00 LAKHS	4.33 LAKHS
2020-24 (updated details)	404	301	74.50	2.42 Lakhs	13.00 Lakhs	4.42 Lakhs
2019-23	414	392	94.69	2.60 Lakhs	8.40 Lakhs	4.82 Lakhs
2018-22	396	382	96.46	2.60 Lakhs	8.40 Lakhs	4.41 Lakhs

17. List of Research Projects / Consultancy Works

Number of Projects carried out, funding agency, Grant received.

From 2022 to 2024

S.No	Description	Name of the Faculty Member	Department	Funding Agency	Sanctioned Amount	Duration	Current Status
1	Smart Antenna Design for 6G Wireless Systems	Dr.K.HelenPrabha Dr.C.Bennila Thangammal Ms. R.M. Senthil Selvi	ECE	DST -SERB-Assistance to Professional bodies and seminar	₹ 1,00,000	2023-2024	Sanctioned
2	Pioneering Adavanced AI techniques for Revolutionary Medical Image Diagnostics	Dr. C.S. Anita Dr. K. Sudharson	AIML	DST -SERB-Assistance to Professional bodies and seminar	₹ 1,00,000	2023-2024	Sanctioned
3	National Level Conference CyberSynapse 23	Dr. G. Amudha	CSBS	DRDO-Conference Grant	₹ 50,000	2023-2024	Completed
4	Anna University Sponsored FDTP on AI -AL3391	Dr.P.Ezhumalai Dr.S.Muthusundari	CSE	AU- Centre for Faculty& Professional Development	₹ 35,000	2023-2024	Completed
5	Suicide Control by the measurment of mass with load cell equiped in fan uisng IoT Technique	Mr. J. Jagan Babu	ECE	TNSCST-Student Project Scheme	₹ 4,000	2022-2023	Completed
6	Music Player Recommendation based on Facial Expression	Dr. M. Vedaraj	CSE	TNSCST-Student Project Scheme	₹ 7,500	2022-2023	Completed

7	Anna University Sponsored FDTP on Pyhton Programming and Problem Solving	Dr.G.Amudha Dr.C.S.Anita	CSBS	Anna University	₹ 27,000	2022-2023	Completed

Click the below link for more details on Research / Consultancy projects Completed/Applied/ Ongoing

https://rmd.ac.in/research/fp.html

Publications (if any) out of research in last three years

S.No	Department	No. of Research publications				
		2024	**2023**	**2022**	**2021**	**2020**
1	CSE	109	72	71	70	19
2	ECE	98	75	54	66	37
5	IT	45	18	19	30	8
6	CSBS	36	25	7	7	-
7	AIML	40	06	9	-	-
8	S&H	104	38	41	24	14
TOTAL		**432**	**234**	**242**	**204**	**215**

Click the below link for the details of the publications

https://rmd.ac.in/research/rp.html

18. MoUs with Industries

S.No.	Name of the Organization	Date of MoU	Period of Validity
1	HCL TECHNOLOGIES LTD	20.01.2023	20.01.2026
2	KAMACHI INDUSTRIES	15.10.2022	15.10.2027
3	MICROSOFT DYNAMIC	15.08.2013	Life Time
4	MITSUBISHI ELECTRIC INDIA PRIVATE LIMITED	1.04.2023	31.03.2026
5	NUCLEUS SATELLITE COMMUNICATIONS MADRAS PVT. LTD.	8.01.2019	8.01.2024
6	SOLITON TECHNOLOGIES PRIVATE LIMITED	18.05.2022	18.05.2027
7	TATA CONSULTANCY SERVICES	27.10.2020	31.05.2025
8	THEJO ENGINEERING	27.02.2020	27.02.2025
9	VIRTUSA CONSULTING SERVICES PRIVATE LIMITED (DATASCIENCE,FULL STACK,CLOUD COMPUTING, FRONT END TECHNOLOGY)	16.07.2021	16.07.2024
10	WESTERN THOMSON	23.08.2020	23.08.2025
11	MITSUBA INDIA PVT LTD.	19.05.2022	19.05.2027
12	TATA ELXSI	21.09.2022	21.09.2024
13	KOBELCO	22.11.2022	22.11.2027
14	NTT DATA	13.03.2023	13.03.2024
15	ATOS	18.05.2023	18.05.2024
16	COGNIZANT	06.09.2023	06.09.2025
17	BAETTR INDIA PVT LTD.	17.12.2023	17.12.2028
18	SRI VENKATACHALAPATHY ALLOYS PVT. LTD.	17.12.2023	17.12.2028
19	JUMBO BAG LTD.	17.12.2023	17.12.2028
20	KIKUWA INDIA PVT. LTD.	17.12.2023	17.12.2028
21	KTV HEALTH FOOD PVT. LTD.	17.12.2023	17.12.2028
22	KPIT TECHNOLOGIES LTD.	27.10.2021	27.10.2023
23	NEC Corporation India Private Limited	17.08.2022	17.08.2023
24	LTIMINDTREE (CSD) - Knowledge Partner	08.04.2024	08.04.2027
25	ORACLE - IT	18.04.2024	18.04.2025
26	EMERALD TYRES	08.03.2024	08.03.2029
27	Vantiva	16.05.2024	16.05.2029